

SPECIFICATION

SPEC. No. A-YFF-a

D A T E : 2017 Jan.

To

Non-Controlled Copy

CUSTOMER'S PRODUCT NAME

TDK'S PRODUCT NAME

3 Terminal Filter
YFF Series/ Automotive grade
(Feed through type)

Please return this specification to TDK representatives with your signature.
If orders are placed without returned specification, please allow us to judge that specification is accepted by your side.

RECEIPT CONFIRMATION

DATE: _____ YEAR _____ MONTH _____ DAY _____

Test conditions in this specification based on AEC-Q200 for Automotive application.

TDK Corporation
Sales
Electronic Components
Sales & Marketing Group

Engineering
Electronic Components Business Company
Ceramic Capacitors Business Group

APPROVED	Person in charge

APPROVED	CHECKED	Person in charge

1. SCOPE

This specification is applicable to 3 terminal filter feed through type with a priority over the other relevant specifications.

Production places defined in this specification shall be TDK Corporation Japan, TDK (Suzhou) Co., Ltd and TDK Components U.S.A. Inc.

EXPLANATORY NOTE:

This specification warrants the quality of the product. The product should be evaluated or confirmed a state of mounted on your product.

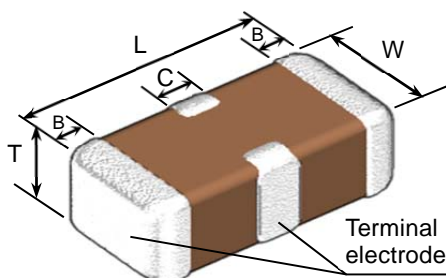
If the use of the chips goes beyond the bounds of the specification, we can not afford to guarantee.

2. CODE CONSTRUCTION

(Example): $\frac{YFF18}{(1)}$ $\frac{AC}{(2)}$ $\frac{1C}{(3)}$ $\frac{104}{(4)}$ $\frac{M}{(5)}$ $\frac{T}{(6)}$ $\frac{0Y0N}{(7)}$

(1) Type

<YFF18AC, YFF21AC, YFF31AH>



*As for dimensions of each product, please refer to detailed information on TDK web.

(2) Product Classification

Symbol	Product Classification
AC	STD 3Terminals for Power Line
AH	For Large Current Power Line

(3) Rated Voltage

Symbol	Rated Voltage
2 A	DC 100 V
1 H	DC 50 V
1 E	DC 25 V
1 C	DC 16 V

(4) Rated Capacitance

Stated in three digits and in units of pico farads (pF).

The first and second digits identify the first and second significant figures of the capacitance, the third digit identifies the multiplier.

Example 221 → 220pF

104 → 100,000pF = 0.1μF

(5) Capacitance tolerance

Symbol	Tolerance
M	±20 %

(6) Packaging

Symbol	Packaging
T	Taping

(7) TDK Internal code

3. RATED CURRENT

As for Rated Current of each product, please refer to detailed information on TDK web.

4. OPERATING TEMPERATURE RANGE

As for Operating Temperature range of each product, please refer to detailed information on TDK web.

5. STORING CONDITION AND TERM

5 to 40°C at 20 to 70%RH

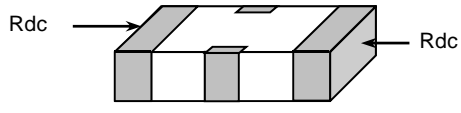
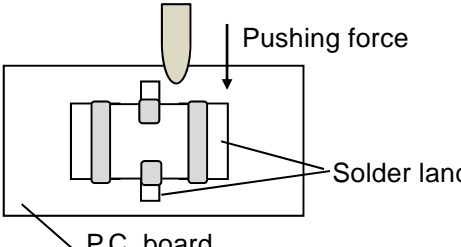
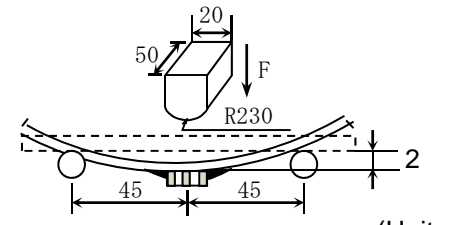
6 months Max.

6. INDUSTRIAL WASTE DISPOSAL

Dispose this product as industrial waste in accordance with the Industrial Waste Law.

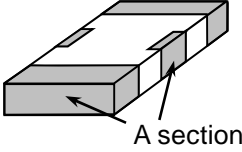
7. PERFORMANCE

table 1

No.	Item	Performance	Test or inspection method				
1	External Appearance	No defects which may affect performance.	Inspect with magnifying glass (3x).				
2	Direct Current Resistance (Rdc)	As for Direct Current spec of each product, please refer to detailed information on TDK web.	Measuring current shall be 100mA max.. 				
3	Capacitance	Within the specified tolerance.	<table border="1"> <thead> <tr> <th>Measuring frequency</th> <th>Measuring voltage</th> </tr> </thead> <tbody> <tr> <td>1kHz±10%</td> <td>1.0±0.2Vrms</td> </tr> </tbody> </table>	Measuring frequency	Measuring voltage	1kHz±10%	1.0±0.2Vrms
Measuring frequency	Measuring voltage						
1kHz±10%	1.0±0.2Vrms						
4	Robustness of Terminations	No sign of termination coming off, breakage of ceramic, or other abnormal signs.	Reflow solder the products on a P.C. board shown in Appendix1 and apply a pushing force of 17.7N. 				
5	Bending	External appearance	Reflow solder the products on a P.C. Board shown in Appendix1 and bend it for 2mm (1mm is applied for YFF31AH type).  (Unit:mm)				
	Capacitance	<table border="1"> <tbody> <tr> <td>Change from the value before test</td> </tr> <tr> <td>±12.5 %</td> </tr> </tbody> </table>		Change from the value before test	±12.5 %		
	Change from the value before test						
±12.5 %							
Resistance for DC (Rdc)	<p>500mΩ max. except for following parts.</p> <p>150mΩmax.: YFF21AC1E103M</p> <p>100mΩmax.: YFF18AC1H103M</p> <p> YFF18AC1E223M</p> <p> YFF21AC1E223M~104M</p> <p>50mΩmax.: YFF18AC1C104M</p> <p> YFF21AC1C474M</p> <p> YFF31AH2A104M,105M</p>						

(continued)

table 1

No.	Item	Performance	Test or inspection method						
6	Solderability	<p>New solder to cover over 75% of termination. 25% may have pin holes or rough spots but not concentrated in one spot. Ceramic surface of A sections shall not be exposed due to melting or shifting of termination material.</p> 	<p>Completely soak both terminations in solder at the following conditions.</p> <p>Solder : Sn-3.0Ag-0.5Cu or Sn-37Pb Temperature : 245±5°C(Sn-3.0Ag-0.5Cu) 235±5°C(Sn-37Pb) Soaking time : 3±0.3s(Sn-3.0Ag-0.5Cu) 2±0.2s(Sn-37Pb)</p> <p>Flux: Isopropyl alcohol (JIS K 8839) Rosin (JIS K 5902) 25% solid solution.</p>						
7	Resistance to solder heat	<table border="1"> <tr> <td>External appearance</td> <td>No cracks are allowed and terminations shall be covered at least 60% with new solder.</td> </tr> <tr> <td>Capacitance</td> <td> <div style="border: 1px solid black; padding: 5px; text-align: center;"> Change from the value before test ±7.5 % </div> </td> </tr> <tr> <td>Resistance for DC (Rdc)</td> <td> 500mΩ max. except for following parts. 150mΩmax.:YFF21AC1E103M 100mΩmax.:YFF18AC1H103M YFF18AC1E223M YFF21AC1E223M~104M 50mΩmax. YFF18AC1C104M,474M YFF31AH2A104M,105M </td> </tr> </table>	External appearance	No cracks are allowed and terminations shall be covered at least 60% with new solder.	Capacitance	<div style="border: 1px solid black; padding: 5px; text-align: center;"> Change from the value before test ±7.5 % </div>	Resistance for DC (Rdc)	500mΩ max. except for following parts. 150mΩmax.:YFF21AC1E103M 100mΩmax.:YFF18AC1H103M YFF18AC1E223M YFF21AC1E223M~104M 50mΩmax. YFF18AC1C104M,474M YFF31AH2A104M,105M	<p>Completely soak both terminations in solder at the following conditions. 260±5°C for 10±1s.</p> <p>Preheating condition Temp.: 110~140°C Time : 30~60s.</p> <p>Solder : Sn-3.0Ag-0.5Cu or Sn-37Pb</p> <p>Flux : Isopropyl alcohol (JIS K 8839) Rosin (JIS K 5902) 25% solid solution.</p> <p>Leave the capacitors in ambient condition for 24±2h before measurement.</p>
External appearance	No cracks are allowed and terminations shall be covered at least 60% with new solder.								
Capacitance	<div style="border: 1px solid black; padding: 5px; text-align: center;"> Change from the value before test ±7.5 % </div>								
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8	Vibration	<table border="1"> <tr> <td>External appearance</td> <td>No mechanical damage.</td> </tr> <tr> <td>Capacitance</td> <td> <div style="border: 1px solid black; padding: 5px; text-align: center;"> Change from the value before test ±7.5 % </div> </td> </tr> <tr> <td>Resistance for DC (Rdc)</td> <td> 500mΩ max. except for following parts. 150mΩmax.:YFF21AC1E103M 100mΩmax.:YFF18AC1H103M YFF18AC1E223M YFF21AC1E223M~104M 50mΩmax. YFF18AC1C104M,474M YFF31AH2A104M,105M </td> </tr> </table>	External appearance	No mechanical damage.	Capacitance	<div style="border: 1px solid black; padding: 5px; text-align: center;"> Change from the value before test ±7.5 % </div>	Resistance for DC (Rdc)	500mΩ max. except for following parts. 150mΩmax.:YFF21AC1E103M 100mΩmax.:YFF18AC1H103M YFF18AC1E223M YFF21AC1E223M~104M 50mΩmax. YFF18AC1C104M,474M YFF31AH2A104M,105M	<p>Reflow Solder the products on a P.C. Board shown in Appendix2 before testing.</p> <p>Vibrate the products with following conditions.</p> <p>Applied force : 5G max. Frequency : 10 - 2,000Hz Duration : 20 min. Cycle : 12 cycles in each 3 mutually perpendicular directions.</p>
External appearance	No mechanical damage.								
Capacitance	<div style="border: 1px solid black; padding: 5px; text-align: center;"> Change from the value before test ±7.5 % </div>								
Resistance for DC (Rdc)	500mΩ max. except for following parts. 150mΩmax.:YFF21AC1E103M 100mΩmax.:YFF18AC1H103M YFF18AC1E223M YFF21AC1E223M~104M 50mΩmax. YFF18AC1C104M,474M YFF31AH2A104M,105M								

(continued)

No.	Item		Performance	Test or inspection method															
9	Temperature cycle	External appearance	No mechanical damage.	<p>Reflow solder the products on a P.C. board shown in Appendix2 before testing.</p> <p>Expose the product in the condition step1 through step 4 and repeat 1,000 times consecutively.</p> <p>Leave the products in ambient condition for 24±2h before measurement.</p> <table border="1"> <thead> <tr> <th>Step</th> <th>Temperature(°C)</th> <th>Time(min.)</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>-55 ± 3</td> <td>30 ± 3</td> </tr> <tr> <td>2</td> <td>25</td> <td>2 - 5</td> </tr> <tr> <td>3</td> <td>125 ± 2</td> <td>30 ± 2</td> </tr> <tr> <td>4</td> <td>25</td> <td>2 - 5</td> </tr> </tbody> </table>	Step	Temperature(°C)	Time(min.)	1	-55 ± 3	30 ± 3	2	25	2 - 5	3	125 ± 2	30 ± 2	4	25	2 - 5
		Step	Temperature(°C)		Time(min.)														
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2	25	2 - 5																	
3	125 ± 2	30 ± 2																	
4	25	2 - 5																	
Capacitance	<p>Change from the value before test</p> <hr/> <p>±7.5 %</p> <hr/>																		
Resistance for DC (Rdc)	<p>500mΩ max. except for following parts.</p> <p>150mΩmax.:YFF21AC1E103M</p> <p>100mΩmax.:YFF18AC1H103M</p> <p>YFF18AC1E223M</p> <p>YFF21AC1E223M~104M</p> <p>50mΩmax. YFF18AC1C104M,474M</p> <p>YFF31AH2A104M,105M</p>																		
10	Moisture Resistance (Steady State)	External appearance	No mechanical damage.	<p>Reflow solder the products on a P.C. board shown in Appendix2 before testing.</p> <p>Leave at temperature 40±2°C, 90 to 95%RH for 500 +24,0h.</p> <p>Leave the products in ambient condition for 24±2h before measurement.</p>															
		Capacitance	<p>Change from the value before test</p> <hr/> <p>±12.5 %</p> <hr/>																
		Resistance for DC (Rdc)	<p>500mΩ max. except for following parts.</p> <p>150mΩmax.:YFF21AC1E103M</p> <p>100mΩmax.:YFF18AC1H103M</p> <p>YFF18AC1E223M</p> <p>YFF21AC1E223M~104M</p> <p>50mΩmax. YFF18AC1C104M,474M</p> <p>YFF31AH2A104M,105M</p>																

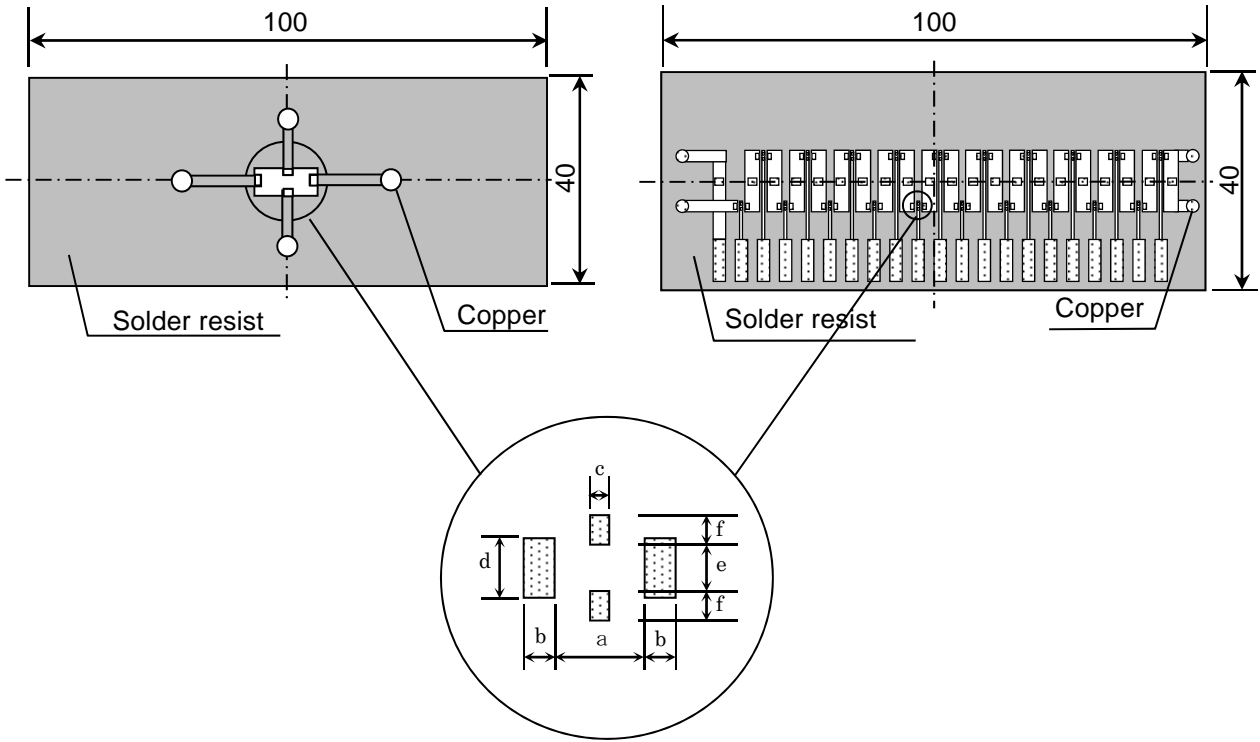
(continued)

No.	Item		Performance	Test or inspection method
11	Moisture Resistance	External appearance	No mechanical damage.	Reflow solder the products on a P.C. board shown in Appendix2 before testing.
		Capacitance	<div style="border: 1px solid black; padding: 2px; text-align: center;"> Change from the value before test $\pm 12.5\%$ </div>	Apply the rated voltage at temperature 85°C and 85%RH for 1,000 +48,0h. Charge/discharge current shall not exceed 50mA.
		Resistance for DC (Rdc)	500mΩ max. except for following parts. 150mΩmax.:YFF21AC1E103M 100mΩmax.:YFF18AC1H103M YFF18AC1E223M YFF21AC1E223M~104M 50mΩmax. YFF18AC1C104M,474M YFF31AH2A104M,105M	Leave the products in ambient condition for 24±2h before measurement. Voltage conditioning: Voltage treat the products under testing temperature and voltage for 1 hour. Leave the products in ambient condition for 24±2h before measurement. Use this measurement for initial value.
12	Life	External Appearance	No mechanical damage.	Reflow solder the products on a P.C. board shown in Appendix2 before testing.
		Capacitance	<div style="border: 1px solid black; padding: 2px; text-align: center;"> Change from the value before test $\pm 15\%$ </div>	Apply the following rated voltage at 125±2°C for 1,000+48,0h. - Rated voltage _{x1} : 1,000pF and over except for YFF31 - Rated voltage _{x2} : Under 1,000pF, YFF31AH2A104M,105M
		Resistance for DC (Rdc)	500mΩ max. except for following parts. 150mΩmax.:YFF21AC1E103M 100mΩmax.:YFF18AC1H103M YFF18AC1E223M YFF21AC1E223M~104M 50mΩmax. YFF18AC1C104M,474M YFF31AH2A104M,105M	Apply the rated current except for following parts. - DC 10A:YFF31AH2A104M - DC 6A: YFF31AH2A105M - DC 4A: YFF18AC1C104M - DC 2A: YFF21AC1E223M~104M YFF21AC1C474M - DC 1A: YFF18AC1H220M~103M YFF18AC1E223M YFF21AC1E103M - DC 0.4A: YFF21AC1H220M~472M Charge/discharge current shall not exceed 50mA. Voltage conditioning: Voltage treat the products under testing temperature and voltage for 1h. Leave the product in ambient condition for 24±2h before measurement. Use this measurement for initial value.

*As for the initial measurement of products on number 5,7,8,9 and 10, leave products at 150 -10,0°C for 1 hour and measure the value after leaving products for 24±2h in ambient condition.

Appendix1 P.C. board for bending test



Appendix2 P.C. board for reliability test



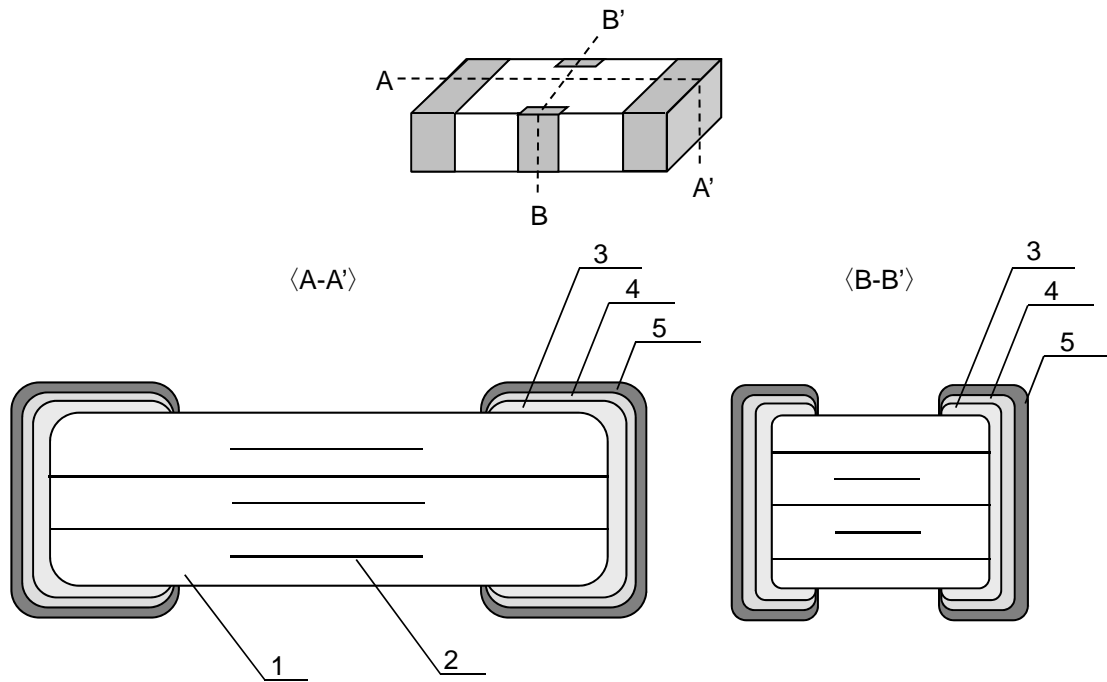
(Unit: mm)

Type	Symbol	a	b	c	d	e	f
	YFF18	1	0.6	0.4	0.6	0.4	0.4
	YFF21	1.4	0.6	0.5	0.8	0.6	0.65
	YFF31	2.5	1.2	1.4	1.3	0.8	0.9

1. Material : Glass Epoxy (As per JIS C6484 GE4)
2. Thickness : 1.6mm

 Copper (Thickness: Others 0.035mm)
 Solder resist

8. INSIDE STRUCTURE AND MATERIAL



No.	NAME	MATERIAL
1	Dielectric	CaZrO ₃ or BaTiO ₃
2	Electrode	Ni
3	Termination	Cu
4		Ni
5		Sn

9. PACKAGING

Packaging shall be done to protect the components from the damage during transportation and storing, and a label which has the following information shall be attached.

Tape packaging is as per TDK tape packaging specification.

- 1) Inspection No.
- 2) TDK P/N
- 3) Customer's P/N
- 4) Quantity

*Composition of Inspection No.

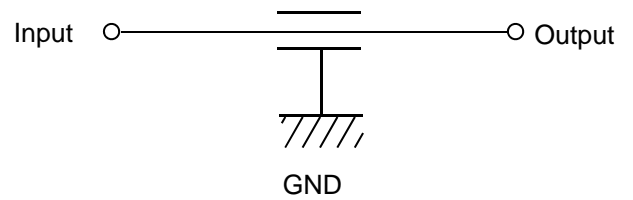
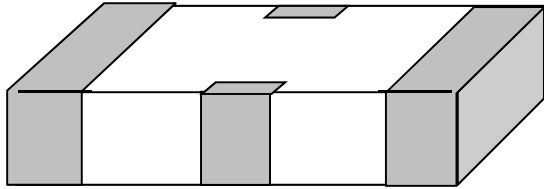
Example F 7 A - 00 - 000
 (a) (b) (c) (d) (e)

- a) Line code
- b) Last digit of the year
- c) Month and A for January and B for February and so on. (Skip I)
- d) Inspection Date of the month.
- e) Serial No. of the day


10. SOLDERING CONDITION


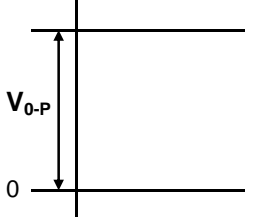
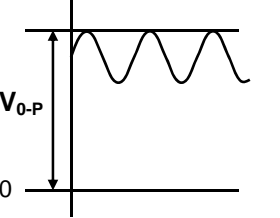
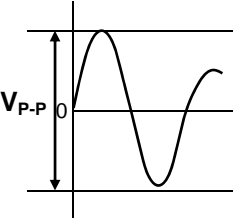
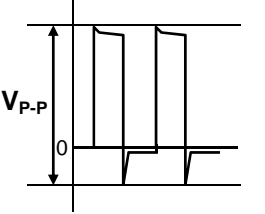
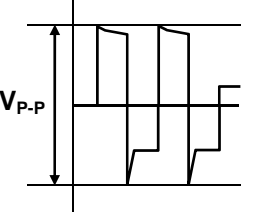
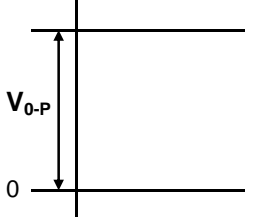
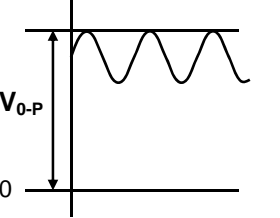
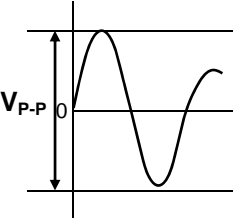
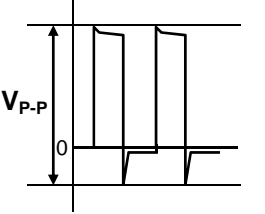
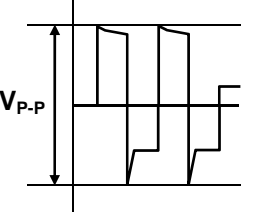
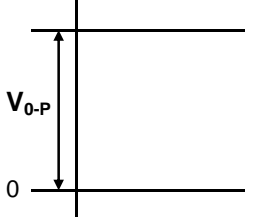
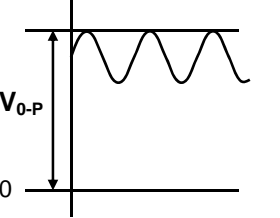
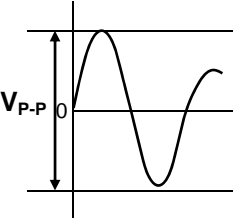
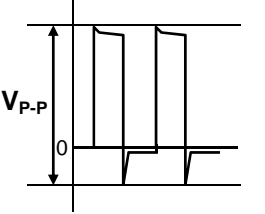
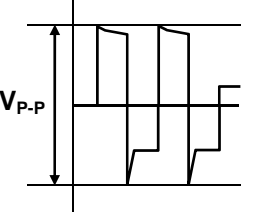
Soldering is limited to Reflow soldering.

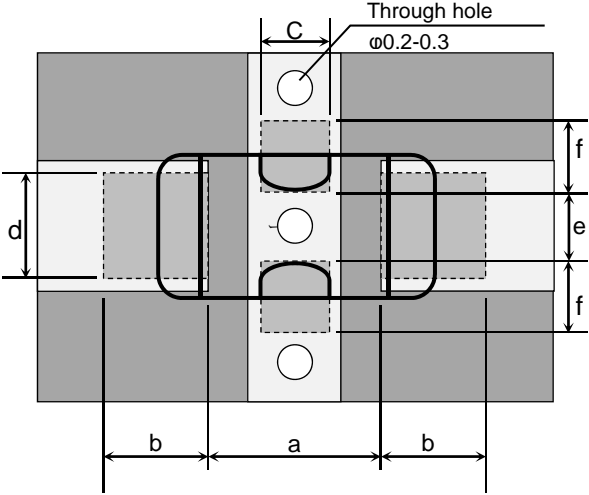
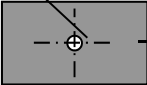
11. EQUIVALENT CIRCUIT DIAGRAM

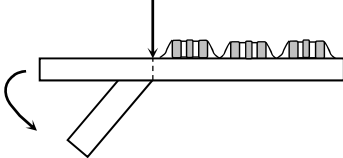
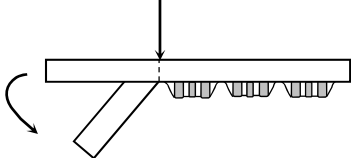
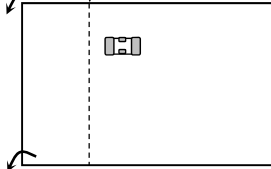
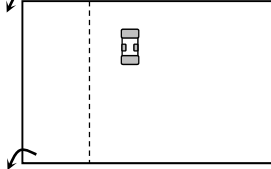
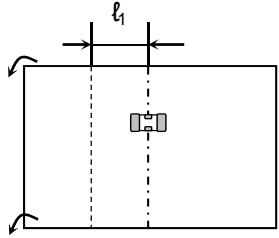
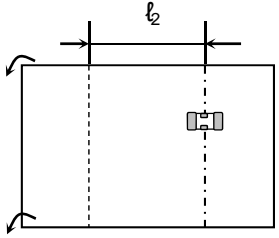


12. Caution

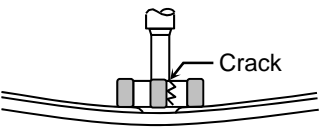
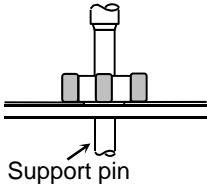
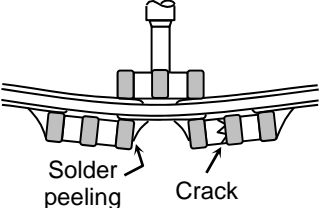
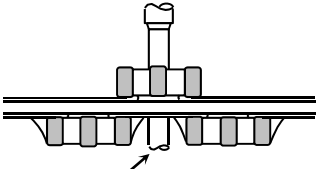
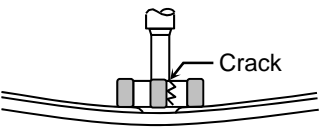
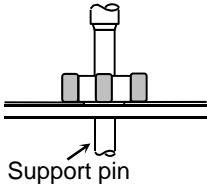
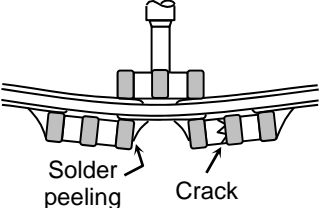
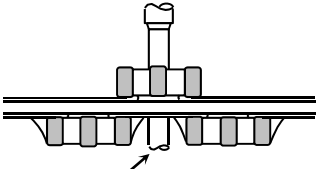
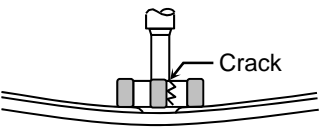
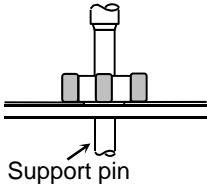
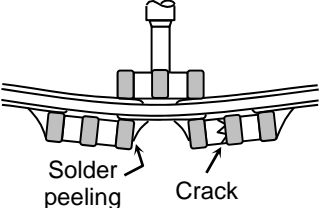
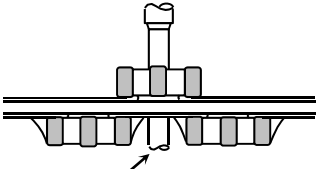
No.	Process	Condition
1	Operating Condition (Storage, Transportation)	<p>1-1. Storage</p> <ol style="list-style-type: none"> 1) The product must be stored in an ambient temperature of 5 to 40°C with a relative humidity of 20 to 70%RH. The products should be used within 6 months upon receipt. 2) The product must be operated and stored in an environment free of dew condensation and these gases such as Hydrogen Sulphide, Hydrogen Sulphate, Chlorine, Ammonia and sulfur. 3) Avoid storing in sun light and falling of dew. 4) Do not use product under high humidity and high and low atmospheric pressure which may affect product reliability. 5) Product should be tested for the solderability when they are stored for long time. <p>1-2. Handling in transportation</p> <p>In case of the transportation of the product, the performance of the product may be deteriorated depending on the transportation condition. (Refer to JEITA RCR-2335C 9.2 Handling in transportation)</p>
2	Circuit design  Caution	<p>2-1. Operating temperature</p> <p>Operating temperature should be followed strictly within this specification, especially be careful with maximum temperature.</p> <ol style="list-style-type: none"> 1) Do not use product above the maximum allowable operating temperature. 2) Surface temperature including self heating should be below maximum operating temperature. (Due to dielectric loss, product will heat itself when AC is applied. Especially at high frequencies around its SRF, the heat might be so extreme that it may damage itself or the product mounted on. Please design the circuit so that the maximum temperature of the product including the self heating to be below the maximum allowable operating temperature. Temperature rise at product's surface shall be below 20°C) 3) The electrical characteristics of the product will vary depending on the temperature. The product should be selected and designed in taking the temperature into consideration.

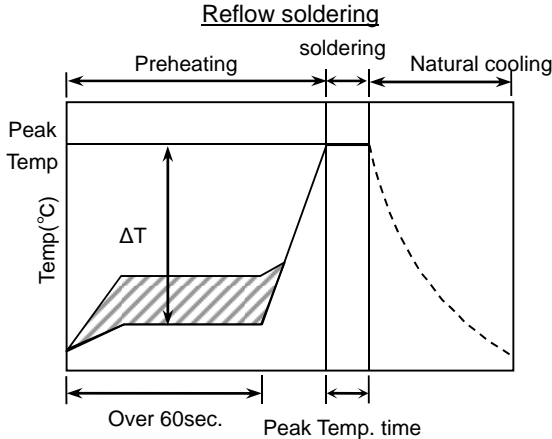
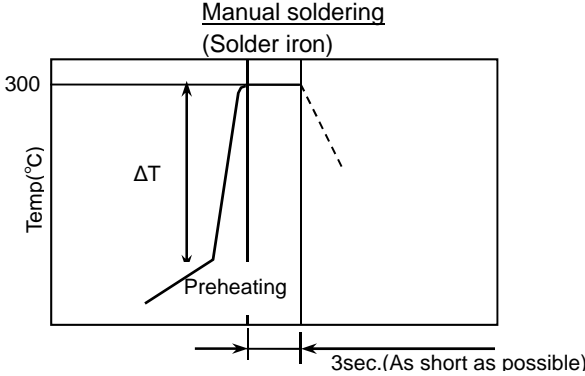
No	Process	Condition																
2	Circuit design  Caution	<p>2-2. Operating voltage</p> <p>1) Operating voltage across the terminals should be below the rated voltage. When AC and DC are super imposed, V_{0-P} must be below the rated voltage. — (1) and (2)</p> <p>AC or pulse with overshooting, V_{P-P} must be below the rated voltage. — (3), (4) and (5)</p> <p>When the voltage is started to apply to the circuit or it is stopped applying, the irregular voltage may be generated for a transit period because of resonance or switching. Be sure to use the product within rated voltage containing these Irregular voltage.</p> <table border="1" data-bbox="427 474 1492 1048"> <thead> <tr> <th data-bbox="427 474 651 517">Voltage</th> <th data-bbox="651 474 938 517">(1) DC voltage</th> <th data-bbox="938 474 1241 517">(2) DC+AC voltage</th> <th data-bbox="1241 474 1492 517">(3) AC voltage</th> </tr> </thead> <tbody> <tr> <td data-bbox="427 517 651 748">Positional Measurement (Rated voltage)</td> <td data-bbox="651 517 938 748">  </td> <td data-bbox="938 517 1241 748">  </td> <td data-bbox="1241 517 1492 748">  </td> </tr> <tr> <th data-bbox="427 779 651 822">Voltage</th> <th data-bbox="651 779 938 822">(4) Pulse voltage (A)</th> <th data-bbox="938 779 1241 822">(5) Pulse voltage (B)</th> <td></td> </tr> <tr> <td data-bbox="427 822 651 1048">Positional Measurement (Rated voltage)</td> <td data-bbox="651 822 938 1048">  </td> <td data-bbox="938 822 1241 1048">  </td> <td></td> </tr> </tbody> </table> <p>2) Even below the rated voltage, if repetitive high frequency AC or pulse is applied, the reliability of the products may be reduced.</p> <p>3) The effective capacitance will vary depending on applied DC and AC voltages. The products should be selected and designed in taking the voltages into consideration.</p> <p>2-3. Frequency</p> <p>When the products are used in AC and/or pulse voltages, the products may vibrate themselves and generate audible sound.</p>	Voltage	(1) DC voltage	(2) DC+AC voltage	(3) AC voltage	Positional Measurement (Rated voltage)				Voltage	(4) Pulse voltage (A)	(5) Pulse voltage (B)		Positional Measurement (Rated voltage)			
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Positional Measurement (Rated voltage)																		
3	Designing P.C. board	<p>The amount of solder at the terminations has a direct effect on the reliability of the products.</p> <p>1) The greater the amount of solder, the higher the stress on the products, and the more likely that it will break. When designing a P.C. board, determine the shape and size of the solder lands to have proper amount of solder on the terminations.</p> <p>2) Avoid using common solder land for multiple terminations and provide individual solder land for each terminations.</p>																

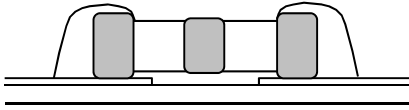
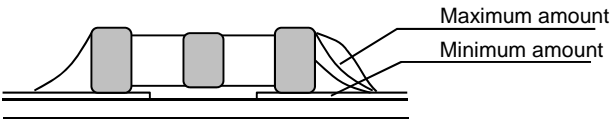
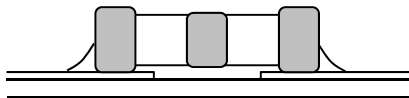
No	Process	Condition																																							
3	Designing P.C.board	<p>3) Size and recommended land dimensions.</p>  <p>Through hole $\phi 0.2-0.3$</p> <p>Connect to the ground pattern of the chip mounted side.</p> <p>"Through hole" should be designed as close to GND terminal as possible.</p> <p>Back side Back side shall be connected to the ground pattern of the chip mounted side. Please design the ground of back side as big as possible.</p> <p>*If through hole is too big, solder paste way came into the hole and make bad connection with the ground pattern.</p> <p>Connect to the ground pattern of the chip mounted side.</p>  <p>Ground pattern</p> <p> Resist Land pattern Land pattern & Resist </p> <table border="1" style="margin-left: auto; margin-right: auto;"> <thead> <tr> <th colspan="2"></th> <th colspan="5" style="text-align: center;">(mm)</th> </tr> <tr> <th style="border: none;">Type</th> <th style="border: none;">Symbol</th> <th>a</th> <th>b</th> <th>c</th> <th>d</th> <th>e</th> <th>f</th> </tr> </thead> <tbody> <tr> <td style="border: none;"></td> <td>YFF18</td> <td>1.0</td> <td>0.6</td> <td>0.4</td> <td>0.6</td> <td>0.4</td> <td>0.4</td> </tr> <tr> <td style="border: none;"></td> <td>YFF21</td> <td>1.4</td> <td>0.6</td> <td>0.5</td> <td>0.8</td> <td>0.6</td> <td>0.65</td> </tr> <tr> <td style="border: none;"></td> <td>YFF31</td> <td>2.5</td> <td>1.2</td> <td>1.4</td> <td>1.3</td> <td>0.8</td> <td>0.9</td> </tr> </tbody> </table>			(mm)					Type	Symbol	a	b	c	d	e	f		YFF18	1.0	0.6	0.4	0.6	0.4	0.4		YFF21	1.4	0.6	0.5	0.8	0.6	0.65		YFF31	2.5	1.2	1.4	1.3	0.8	0.9
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No	Process	Condition	
3	Designing P.C. board	4) Recommended product layout is as following.	
		Disadvantage against bending stress	Advantage against bending stress
Mounting face	<p data-bbox="766 371 1037 405">Perforation or slit</p>  <p data-bbox="718 593 981 660">Break P.C. board with mounted side up.</p>	<p data-bbox="1252 371 1460 405">Perforation or slit</p>  <p data-bbox="1149 593 1412 660">Break P.C. board with mounted side down.</p>	
Chip arrangement (Direction)	<p data-bbox="766 797 1005 831">Perforation or slit</p> 	<p data-bbox="1212 797 1452 831">Perforation or slit</p> 	
Distance from slit	<p data-bbox="694 1099 1029 1133">Closer to slit is higher stress</p>  <p data-bbox="989 1467 1077 1512">$(l_1 < l_2)$</p>	<p data-bbox="1125 1099 1460 1133">Away from slit is less stress</p>  <p data-bbox="1388 1467 1476 1512">$(l_1 < l_2)$</p>	


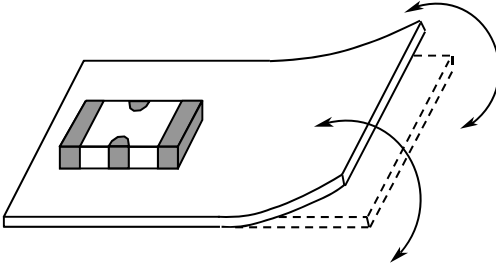
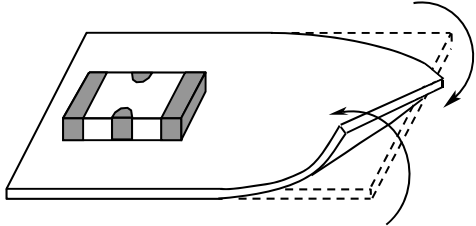
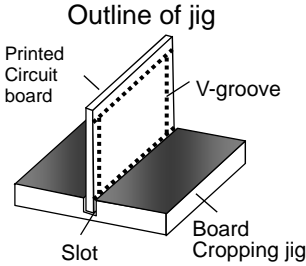
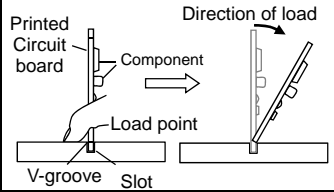
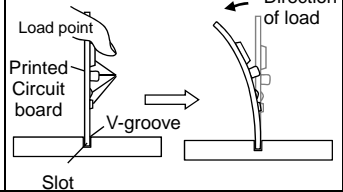
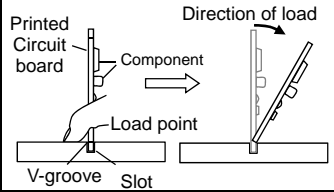
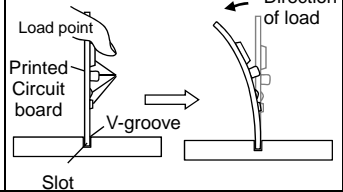
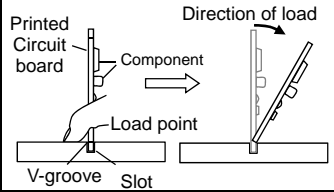
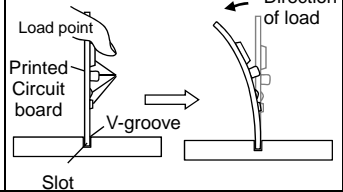
No	Process	Condition									
3	Designing P.C. board	<p data-bbox="475 152 1417 185">5) Mechanical stress varies according to location of product on the P.C. board.</p> <div data-bbox="564 203 1406 763" style="text-align: center;"> </div> <p data-bbox="874 779 1430 842" style="text-align: center;">The stress in products is in the following order. A > B = C > D > E</p> <p data-bbox="475 869 815 902">6) Layout recommendation</p> <table border="1" data-bbox="469 913 1567 1827"> <thead> <tr> <th data-bbox="469 913 719 1032">Example</th> <th data-bbox="719 913 1129 1032">Use of common solder land</th> <th data-bbox="1129 913 1567 1032">Soldering with chassis</th> </tr> </thead> <tbody> <tr> <td data-bbox="469 1032 719 1413">Need to avoid</td> <td data-bbox="719 1032 1129 1413"> </td> <td data-bbox="1129 1032 1567 1413"> </td> </tr> <tr> <td data-bbox="469 1413 719 1827">Recommendation</td> <td data-bbox="719 1413 1129 1827"> </td> <td data-bbox="1129 1413 1567 1827"> </td> </tr> </tbody> </table>	Example	Use of common solder land	Soldering with chassis	Need to avoid			Recommendation		
Example	Use of common solder land	Soldering with chassis									
Need to avoid											
Recommendation											

No	Process	Condition									
4	Mounting	<p>4-1. Stress from mounting head</p> <p>If the mounting head is adjusted too low, it may induce excessive stress in the product to result in cracking. Please take following precautions.</p> <ol style="list-style-type: none"> 1) Adjust the bottom dead center of the mounting head to reach on the P.C. board surface and not press it. 2) Adjust the mounting head pressure to be 1 to 3N of static weight. 3) To minimize the impact energy from mounting head, it is important to provide support from the bottom side of the P.C. board. <p>See following examples.</p> <table border="1" data-bbox="384 577 1337 1137"> <thead> <tr> <th data-bbox="384 577 595 629"></th> <th data-bbox="595 577 965 629">Not recommended</th> <th data-bbox="965 577 1337 629">Recommended</th> </tr> </thead> <tbody> <tr> <td data-bbox="384 629 595 875">Single sided mounting</td> <td data-bbox="595 629 965 875">  <p>Crack</p> </td> <td data-bbox="965 629 1337 875">  <p>Support pin</p> </td> </tr> <tr> <td data-bbox="384 875 595 1137">Double-sides mounting</td> <td data-bbox="595 875 965 1137">  <p>Solder peeling</p> <p>Crack</p> </td> <td data-bbox="965 875 1337 1137">  <p>Support pin</p> </td> </tr> </tbody> </table> <p>When the centering jaw is worn out, it may give mechanical impact on the product to cause crack. Please control the close up dimension of the centering jaw and provide sufficient preventive maintenance and replacement of it.</p>		Not recommended	Recommended	Single sided mounting	 <p>Crack</p>	 <p>Support pin</p>	Double-sides mounting	 <p>Solder peeling</p> <p>Crack</p>	 <p>Support pin</p>
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Single sided mounting	 <p>Crack</p>	 <p>Support pin</p>									
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No.	Process	Condition														
5	Soldering	<p>5-1. Flux selection</p> <p>Although highly-activated flux gives better solderability, substances which increase activity may also have a serious effect on the product. To avoid such degradation, it is recommended following.</p> <ol style="list-style-type: none"> 1) It is recommended to use a mildly activated rosin flux (less than 0.1wt% chlorine). Strong flux is not recommended. 2) Excessive flux must be avoided. Please provide proper amount of flux. 3) When water-soluble flux is used, enough washing is necessary. <p>5-2. Recommended soldering profile by various methods</p> <div style="text-align: center;"> <p><u>Reflow soldering</u></p>  </div> <div style="text-align: center; margin-top: 20px;"> <p><u>Manual soldering</u> (Solder iron)</p>  </div> <p>5-3. Recommended soldering peak temp and peak temp duration</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th rowspan="2" style="text-align: center;">Temp./Duration</th> <th colspan="2" style="text-align: center;">Reflow soldering</th> </tr> <tr> <th style="text-align: center;">Peak temp(°C)</th> <th style="text-align: center;">Duration(sec.)</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">Solder</td> <td></td> <td></td> </tr> <tr> <td style="text-align: center;">Sn-Pb Solder</td> <td style="text-align: center;">230 max.</td> <td style="text-align: center;">20 max.</td> </tr> <tr> <td style="text-align: center;">Lead Free Solder</td> <td style="text-align: center;">260 max.</td> <td style="text-align: center;">10 max.</td> </tr> </tbody> </table> <p>Recommended solder compositions Sn-37Pb (Sn-Pb solder) Sn-3.0Ag-0.5Cu (Lead Free Solder)</p>	Temp./Duration	Reflow soldering		Peak temp(°C)	Duration(sec.)	Solder			Sn-Pb Solder	230 max.	20 max.	Lead Free Solder	260 max.	10 max.
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5	Soldering	<p>5-4. Avoiding thermal shock</p> <p>1) Preheating condition</p> <table border="1" data-bbox="568 230 1003 389"> <thead> <tr> <th>Soldering</th> <th>Temp. (°C)</th> </tr> </thead> <tbody> <tr> <td>Reflow soldering</td> <td>$\Delta T \leq 150$</td> </tr> <tr> <td>Manual soldering</td> <td>$\Delta T \leq 150$</td> </tr> </tbody> </table> <p>2) Cooling condition Natural cooling using air is recommended. If the chips are dipped into a solvent for cleaning, the temperature difference (ΔT) must be less than 100°C.</p> <p>5-5. Amount of solder</p> <p>Excessive solder will induce higher tensile force in product when temperature changes and it may result in chip cracking. In sufficient solder may detach the product from the P.C. board.</p> <div style="display: flex; justify-content: space-between; align-items: flex-start;"> <div style="width: 30%;">Excessive solder</div> <div style="width: 35%; text-align: center;">  </div> <div style="width: 30%;">Higher tensile force in product to cause crack</div> </div> <hr/> <div style="display: flex; justify-content: space-between; align-items: center;"> <div style="width: 30%;">Adequate</div> <div style="width: 35%; text-align: center;">  </div> </div> <hr/> <div style="display: flex; justify-content: space-between; align-items: flex-start;"> <div style="width: 30%;">Insufficient solder</div> <div style="width: 35%; text-align: center;">  </div> <div style="width: 30%;">Low robustness may cause contact failure or product come off the P.C. board.</div> </div> <hr/> <p>5-6. Solder repair by solder iron</p> <p>1) Selection of the soldering iron tip Tip temperature of solder iron varies by its type, P.C. board material and solder land size. The higher the tip temperature, the quicker the operation. However, heat shock may cause a crack in the product. Please make sure the tip temp. before soldering and keep the peak temp and time in accordance with following recommended condition. (Please preheat the product with the condition in 5-4 to avoid the thermal shock.)</p> <p style="text-align: center;">Recommended solder iron condition (Sn-Pb Solder and Lead Free Solder)</p> <table border="1" data-bbox="568 1585 1406 1693"> <thead> <tr> <th>Temp. (°C)</th> <th>Duration (sec.)</th> <th>Wattage (W)</th> <th>Shape (mm)</th> </tr> </thead> <tbody> <tr> <td>300 max.</td> <td>3 max.</td> <td>20 max.</td> <td>Ø 3.0 max.</td> </tr> </tbody> </table> <p>2) Direct contact of the soldering iron with ceramic dielectric of product may cause crack. Do not touch the ceramic dielectric and the terminations by solder iron.</p>	Soldering	Temp. (°C)	Reflow soldering	$\Delta T \leq 150$	Manual soldering	$\Delta T \leq 150$	Temp. (°C)	Duration (sec.)	Wattage (W)	Shape (mm)	300 max.	3 max.	20 max.	Ø 3.0 max.
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No.	Process	Condition
5	Soldering	<p>5-7. Sn-Zn solder Sn-Zn solder affects product reliability. Please contact TDK in advance when utilize Sn-Zn solder.</p> <p>5-8. Countermeasure for tombstone The misalignment between the mounted positions of the products and the land patterns should be minimized. The tombstone phenomenon may occur especially the products are mounted (in longitudinal direction) in the same direction of the reflow soldering. (Refer to JEITA RCR-2335C Annex A (Informative) Recommendations to prevent the tombstone phenomenon)</p>
6	Cleaning	<p>1) If an unsuitable cleaning fluid is used, flux residue or some foreign articles may stick to product surface to deteriorate especially the insulation resistance.</p> <p>2) If cleaning condition is not suitable, it may damage the product.</p> <p>2)-1. Insufficient washing</p> <p>(1) Terminal electrodes may corrode by Halogen in the flux.</p> <p>(2) Halogen in the flux may adhere on the surface of product, and lower the insulation resistance.</p> <p>(3) Water soluble flux has higher tendency to have above mentioned problems (1) and (2).</p> <p>2)-2. Excessive washing</p> <p>When ultrasonic cleaning is used, excessively high ultrasonic energy output can affect the connection between the ceramic product body and the terminal electrode. To avoid this, following is the recommended condition.</p> <p style="text-align: center;">Power : 20 W/lmax. Frequency : 40 kHz max. Washing time : 5 minutes max.</p> <p>2)-3. If the cleaning fluid is contaminated, density of Halogen increases, and it may bring the same result as insufficient cleaning.</p>
7	Coating and molding of the P.C. board	<p>1) When the P.C. board is coated, please verify the quality influence on the product.</p> <p>2) Please verify carefully that there is no harmful decomposing or reaction gas emission during curing which may damage the product.</p> <p>3) Please verify the curing temperature.</p>

No.	Process	Condition				
8	Handling after product mounted  Caution	<p>1) Please pay attention not to bend or distort the P.C. board after soldering in handling otherwise the product may crack.</p> <div style="display: flex; justify-content: space-around; align-items: center;"> <div style="text-align: center;"> <p>Bend</p>  </div> <div style="text-align: center;"> <p>Twist</p>  </div> </div> <p>2) Printed circuit board cropping should not be carried out by hand, but by using the proper tooling. Printed circuit board cropping should be carried out using a board cropping jig as shown in the following figure or a board cropping apparatus to prevent inducing mechanical stress on the board.</p> <p>(1) Example of a board cropping jig</p> <p><u>Recommended example:</u> The board should be pushed from the back side, close to the cropping jig so that the board is not bent and the stress applied to the capacitor is compressive.</p> <p><u>Unrecommended example:</u> If the pushing point is far from the cropping jig and the pushing direction is from the front side of the board, large tensile stress is applied to the capacitor, which may cause cracks.</p> <div style="display: flex; justify-content: space-around; align-items: center;"> <div style="text-align: center;"> <p>Outline of jig</p>  </div> <table border="1" style="border-collapse: collapse;"> <thead> <tr> <th data-bbox="751 1043 1086 1093">Recommended</th> <th data-bbox="1086 1043 1430 1093">Unrecommended</th> </tr> </thead> <tbody> <tr> <td data-bbox="751 1093 1086 1301">  </td> <td data-bbox="1086 1093 1430 1301">  </td> </tr> </tbody> </table> </div>	Recommended	Unrecommended		
Recommended	Unrecommended					
						

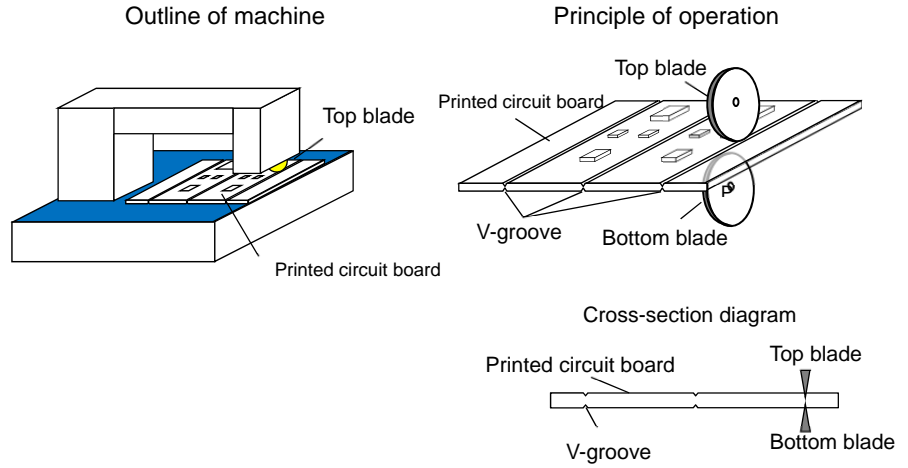
No.	Process	Condition
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8 Handling after chip mounted
 ⚠ Caution

(2) Example of a board cropping machine

An outline of a printed circuit board cropping machine is shown below. The top and bottom blades are aligned with one another along the lines with the V-grooves on printed circuit board when cropping the board.

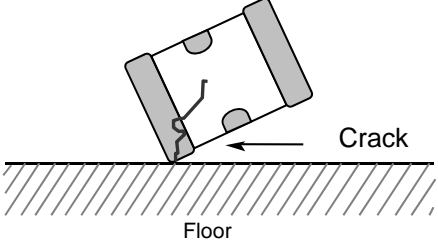
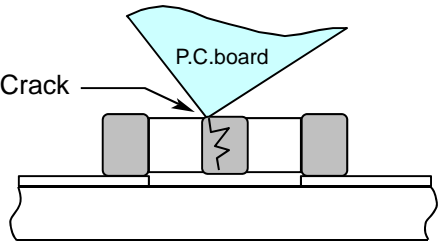
Unrecommended example: Misalignment of blade position between top and bottom, right and left, or front and rear blades may cause a crack in the product.




Recommended	Unrecommended		
	Top-bottom misalignment	Left-right misalignment	Front-rear misalignment
<p>Top blade</p> <p>Board</p> <p>Bottom blade</p>	<p>Top blade</p> <p>Bottom blade</p>	<p>Top blade</p> <p>Bottom blade</p>	<p>Top blade</p> <p>Bottom blade</p>

3) When functional check of the P.C. board is performed, check pin pressure tends to be adjusted higher for fear of loose contact. But if the pressure is excessive and bend the P.C. board, it may crack the product or peel the terminations off. Please adjust the check pins not to bend the P.C. board.

Item	Not recommended	Recommended
Board bending	<p>Termination peeling</p> <p>Check pin</p>	<p>Support pin</p> <p>Check pin</p>

No.	Process	Condition
9	Handling of loose product	<p>1) If dropped the product may crack. Once dropped do not use it. Especially, the large case sized products are tendency to have cracks easily, so please handle with care.</p>  <p>2) Piling the P.C.board after mounting for storage or handling, the corner of the P.C. board may hit the product of another board to cause crack.</p> 
10	Capacitance aging	The products have aging in the capacitance. They may not be used in precision time constant circuit. In case of the time constant circuit, the evaluation should be done well.
11	Estimated life and estimated failure rate of product	<p>As per the estimated life and the estimated failure rate depend on the temperature and the voltage. This can be calculated by the equation described in JEITA RCR-2335C Annex F (Informative) Calculation of the estimated lifetime and the estimated failure rate (Voltage acceleration coefficient : 3 multiplication rule, Temperature acceleration coefficient : 10°C rule)</p> <p>The failure rate can be decreased by reducing the temperature and the voltage but they will not be guaranteed.</p>

No.	Process	Condition
12	Caution during operation of equipment	<p>1) A product shall not be touched directly with bare hands during operation in order to avoid electric shock. Electric energy held by the product may be discharged through the human body when touched with a bare hand. Even when the equipment is off, a product may stay charged. The product should be handled after being completely discharged using a resistor.</p> <p>2) The terminals of a product shall not be short-circuited by any accidental contact with a conductive object. A product shall not be exposed to a conductive liquid such as an acid or alkali solution. A conductive object or liquid, such as acid and alkali, between the terminals may lead to the breakdown of a product due to short circuit</p> <p>3) Confirm that the environment to which the equipment will be exposed during transportation and operation meets the specified conditions. Do not to use the equipment in the following environments.</p> <p>(1) Environment where a product is splattered with water or oil (2) Environment where a product is exposed to direct sunlight (3) Environment where a product is exposed to Ozone, ultraviolet rays or radiation (4) Environment where a product exposed to corrosive gas (e.g. hydrogen sulfide, sulfur dioxide, chlorine, ammonia gas etc.) (5) Environment where a product exposed to vibration or mechanical shock exceeding the specified limits. (6) Atmosphere change with causes condensation</p>
13	Others  Caution	<p>The products listed on this specification sheet are intended for use in general electronic equipment (AV equipment, telecommunications equipment, home appliances, amusement equipment, computer equipment, personal equipment, office equipment, measurement equipment, industrial robots) under a normal operation and use condition.</p> <p>The products are not designed or warranted to meet the requirements of the applications listed below, whose performance and/or quality require a more stringent level of safety or reliability, or whose failure, malfunction or trouble could cause serious damage to society, person or property. Please understand that we are not responsible for any damage or liability caused by use of the products in any of the applications below or for any other use exceeding the range or conditions set forth in this specification sheet. If you intend to use the products in the applications listed below or if you have special requirements exceeding the range or conditions set forth in this specification, please contact us.</p> <p>(1) Aerospace/Aviation equipment (2) Transportation equipment (Electric trains, Ships, etc. except for Automotive applications) (3) Medical equipment (Excepting Pharmaceutical Affairs Law classification Class1, 2) (4) Power-generation control equipment (5) Atomic energy-related equipment (6) Seabed equipment (7) Transportation control equipment (8) Public information-processing equipment (9) Military equipment (10) Electric heating apparatus, burning equipment (11) Disaster prevention/crime prevention equipment (12) Safety equipment (13) Other applications that are not considered general-purpose applications</p> <p>When designing your equipment even for general-purpose applications, you are kindly requested to take into consideration securing protection circuit/device or providing backup circuits in your equipment.</p>

13. TAPE PACKAGING SPECIFICATION

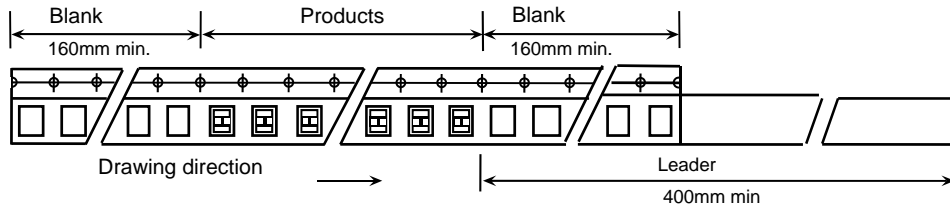
1. CONSTRUCTION AND DIMENSION OF TAPING

1-1. Dimensions of carrier tape

Dimensions of paper tape shall be according to Appendix 2.

Dimensions of plastic tape shall be according to Appendix 3.

1-2. Bulk part and leader of taping



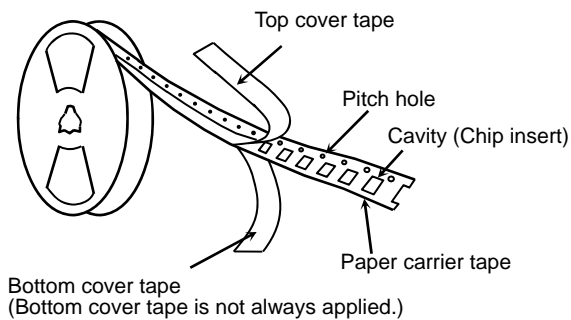
1-3. Dimensions of reel

Dimensions of ϕ 178 reel shall be according to Appendix 4.

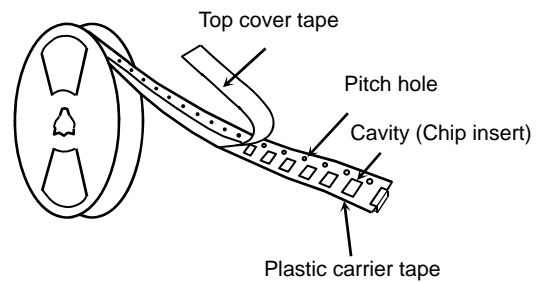
Dimensions of ϕ 330 reel shall be according to Appendix 5.

1-4. Structure of taping

Type 1



Type 2



2. PRODUCT QUANTITY

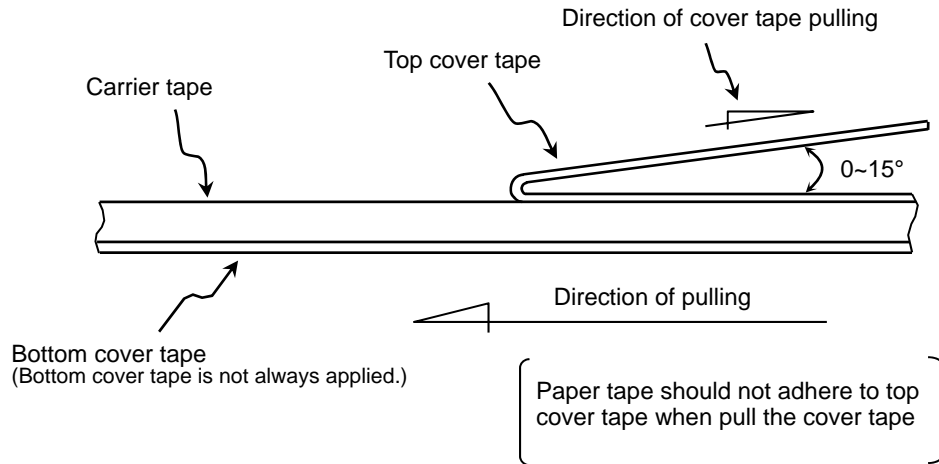
As for product quantity and taping material of each product, please refer to detailed information on TDK web.

3. PERFORMANCE SPECIFICATIONS

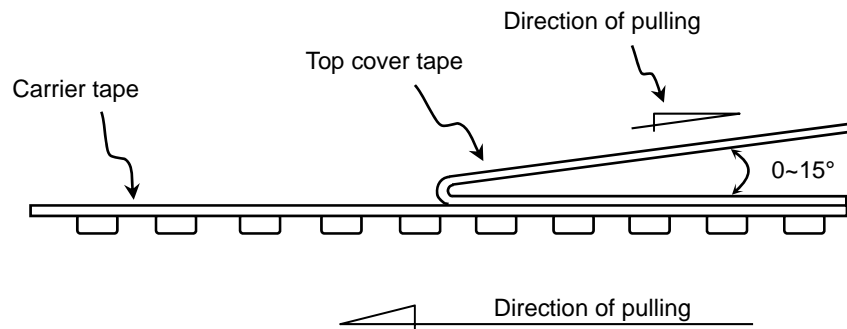
3-1. Fixing peeling strength (top tape)

0.05-0.7N. (See the following figure.)

TYPE 1 (Paper)



TYPE 2 (Plastic)



3-2. Carrier tape shall be flexible enough to be wound around a minimum radius of 30mm with components in tape.

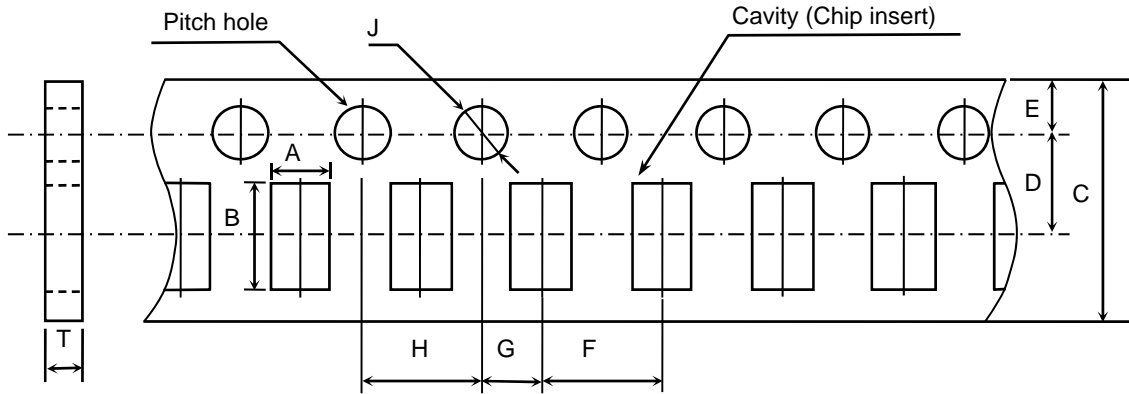
3-3. The missing of components shall be less than 0.1%

3-4. Components shall not stick to fixing tape.

3-5. The fixing tapes shall not protrude beyond the edges of the carrier tape nor shall cover the sprocket holes.

Appendix 2

Paper Tape



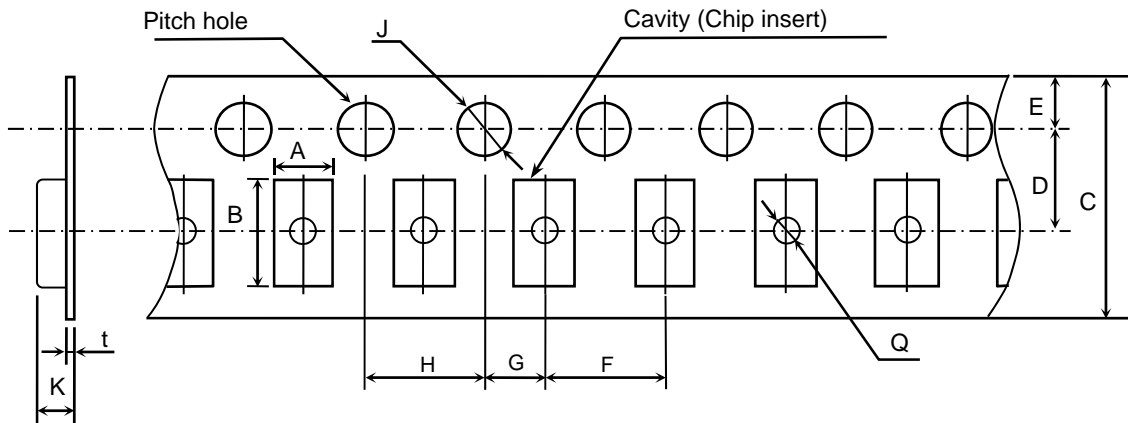
(Unit : mm)

Symbol Type	A	B	C	D	E	F
YFF18	1.10 typ.	1.90 typ.	8.00±0.30	3.50±0.05	1.75±0.10	4.00±0.10
YFF21	1.50 typ.	2.30 typ.				

Symbol Type	G	H	J	T
YFF18	2.00±0.05	4.00±0.10	∅ 1.5 ^{+0.10} ₀	1.2 max.
YFF21				

Appendix 3

Plastic Tape



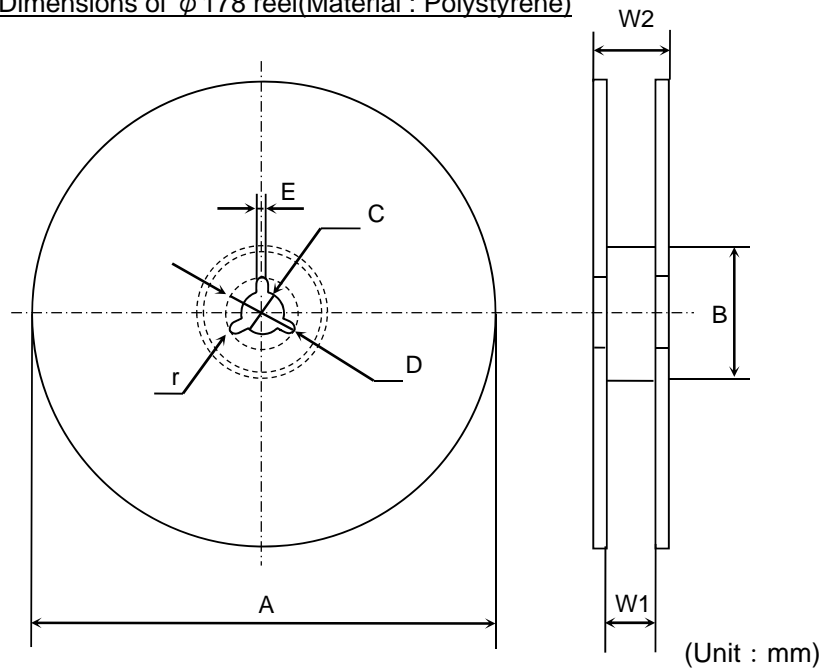
(Unit : mm)

Symbol Type	A	B	C	D	E	F
YFF31	1.90 typ.	3.50 typ.	8.00±0.30	3.50±0.05	1.75±0.10	4.00±0.10

Symbol Type	G	H	J	K	t	Q
YFF31	2.00±0.05	4.00±0.10	∅ 1.5 ^{Top}	2.5 max.	0.3 max.	∅ 0.5 min.

Appendix 4

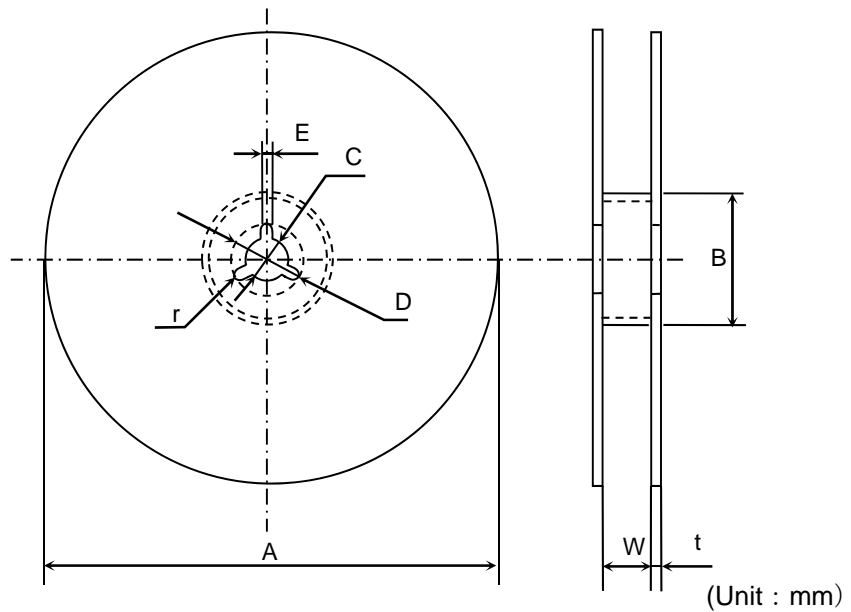
Dimensions of ϕ 178 reel (Material : Polystyrene)



Symbol	A	B	C	D	E	W1
Dimension	ϕ 178 \pm 2.0	ϕ 60 \pm 2.0	ϕ 13 \pm 0.5	ϕ 21 \pm 0.8	2.0 \pm 0.5	9.0 \pm 0.3
Symbol	W2	r				
Dimension	13.0 \pm 1.4	1.0				

Appendix 5

Dimensions of ϕ 330 reel (Material : Polystyrene)



Symbol	A	B	C	D	E	W
Dimension	ϕ 382 max. (Nominal ϕ 330)	ϕ 50 min.	ϕ 13 \pm 0.5	ϕ 21 \pm 0.8	2.0 \pm 0.5	10.0 \pm 1.5
Symbol	t	r				
Dimension	2.0 \pm 0.5	1.0				