

Overview

% T T P M G E X M S R W

The KEMET ESS single-ended aluminum electrolytic capacitors are designed for applications requiring a low TVS ¼ P I Q M R M E X Y V I W S P Y X M S R

Typical applications include portable micro computers, disk drives, calculators and audio equipment.

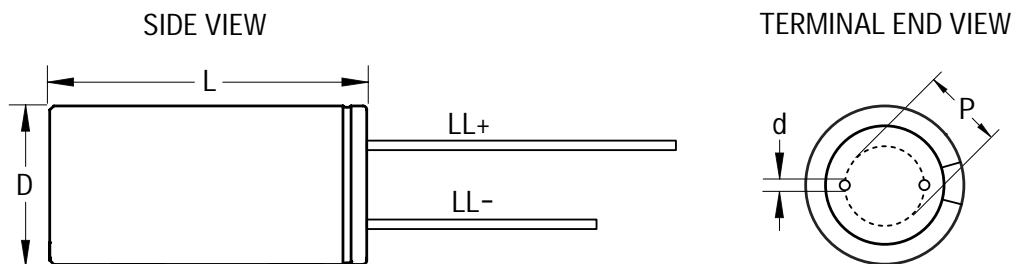
& I R I ¼ X W

- 1,000 hour operating life
- Operating temperature of up to 105°C
- 7 – 9 mm case height
- Safety vent on the capacitor base

4 E V X 2 Y Q F I V 7] W X I Q

ESS	336	M	004		A	B2	AA
Series	Capacitance Code (pF)	Tolerance	Rated Voltage (VDC)		Electrical Parameters	Size Code	Packaging
Single-Ended Aluminum Electrolytic	First two digits represent W M K R M ¼ G E R X ¼ K Y V I W capacitance values. Last H M K M X W T I G M ¼ I W the number of zeros to be added.	M = ±20%	004 = 4 6R3 = 6.3 010 = 10 016 = 16	025 = 25 035 = 35 050 = 50 063 = 63	A = Standard	See Dimension Table	See Ordering Options Table

(M Q I R W M S R W • 1 M P P M Q I X I V W



7 M ^ I ' S H I	D		L		p		d		0 0 0 0 ^ -	
	Nominal	Tolerance	Nominal	Tolerance	Nominal	Tolerance	Nominal	Tolerance	Nominal	Tolerance
B2	4	±0.5	7	-	1.5	±0.5	0.45	Nominal	20/15	Minimum
C2	5	±0.5	7	-	2	±0.5	0.45	Nominal	20/15	Minimum
E2	6.3	±0.5	7	-	2.5	±0.5	0.45	Nominal	20/15	Minimum
G1	8	±0.5	7	-	3.5	±0.5	0.5	Nominal	20/15	Minimum
G2	8	±0.5	9	-	3.5	±0.5	0.5	Nominal	20/15	Minimum

4 I V J S V Q E R G I ' L E V E G X I V M W X M G W

- X I Q	4 I V J S V Q E R G I ' L E V E G X I V M W X M G W
Capacitance Range	1 – 470 µF
Capacitance Tolerance	±20% at 120 Hz/20°C
Rated Voltage	4 – 63 VDC
Life Test	1,000 hours (see conditions in Test Method & Performance)
Operating Temperature	- q' X S q'
Leakage Current	- µ : S V v % [L M G L I Z I V M W K V I E X I V

C = rated capacitance (µF), V = rated voltage (VDC). Voltage applied for 2 minutes at 20°C.

- Q T I H E R G I > ' L E V E G X I V M W X M G W E X , ^

Rated Voltage (VDC)	4	6	10	16	25	35	50	63
> - q' >	q' 6	4	3	2	2	2	2	2
> - q' >	q' 12	8	6	4	4	3	3	3

8EFPI • 6EXMRKW 4EVX 2YQFIV 6IJIVIRGI GSRX H

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[Grey background]	[Grey background]	[Grey background]	[Grey background]	[Grey background]	[Grey background]	[Grey background]	[Grey background]
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(1) Insert packaging code. See Ordering Options Table for available options.

1SYR XMRK 4SWM XMSRW 7EJIX] :IRX

In operation, electrolytic capacitors will always conduct a leakage current that causes electrolysis. The oxygen produced by electrolysis will regenerate the dielectric layer but, at the same time, the hydrogen released may cause the internal pressure of the capacitor to increase. The overpressure vent (safety vent) ensures that the gas can escape when the pressure reaches a certain value. All mounting positions must allow the safety vent to work properly.

- R W X E P P M R K

- A general principle is that lower-use temperatures result in a longer, useful life of the capacitor. For this reason, it should be ensured that electrolytic capacitors are placed away from heat-emitting components. Adequate space should be allowed between components for cooling air to circulate, particularly when high ripple current loads are applied. In any case, the maximum category temperature must not be exceeded.
- Do not deform the case of capacitors or use capacitors with a deformed case.
- Verify that the connections of the capacitors are able to insert on the board without excessive mechanical force.
- If the capacitors require mounting through additional means, the recommended mounting accessories shall be used.
- Verify the correct polarization of the capacitor on the board.
- Verify that the space around the pressure relief device is according to the following guideline:

EWI (MEQIX) IV	7TEGI % VSYRH 7EJIX] :IRX
μ QQ	> 2 mm
" XS μ QQ	> 3 mm
> 40 mm	> 5 mm

It is recommended that capacitors always be mounted with the safety device uppermost or in the upper part of the capacitor.

- -J XLI GETEGMXSVW EVI WXS VIH JSV E PSRK XMQI XLI PIEOEKI GYV
value listed in this catalog, the capacitors must be reformed. In this case, they can be reformed by application of the ZSPXEKI XLVSYKL E WIVMIW VIWMWXS_r $\bar{\mu}$ TTVS \ M Q \bar{E} X W P M W XOS VJ SEVR \bar{B} E T rated voltages.
- In the case of capacitors connected in a series, a suitable voltage sharing must be used.
In the case of balancing resistors, the approximate resistance value can be calculated as: $R = 60/C$.

KEMET recommends, nevertheless, to ensure that the voltage across each capacitor does not exceed its rated voltage.

Equivalent Series Resistance (ESR) and Equivalent Series Capacitance (ESC)

ESR is the resistance in the series circuit of an electrolytic capacitor. It is determined by the physical characteristics of the capacitor, such as the length and cross-sectional area of the leads, the thickness of the electrolyte, and the surface area of the electrodes. ESR is measured in ohms (Ω) and is typically in the range of 0.1 to 100 Ω.

Simplified equivalent circuit diagram of an electrolytic capacitor

The capacitive component of the equivalent series circuit, (equivalent series capacitance - ESC), is determined by applying a voltage across the capacitor and measuring the current. ESC is measured in farads (F) and is typically in the range of 100 pF to 100,000 μF.

ESR and ESC are both important parameters in determining the performance of an electrolytic capacitor.

Capacitance of an electrolytic capacitor depends upon temperature: with decreasing temperature the viscosity of the electrolyte increases, thereby reducing its conductivity.

Capacitance will decrease if temperature decreases. Furthermore, temperature drifts cause armature dilatation and, therefore, capacitance changes (up to 20% depending on the series considered, from 0 to 80°C). This phenomenon is evident for electrolytic capacitors than for other types.

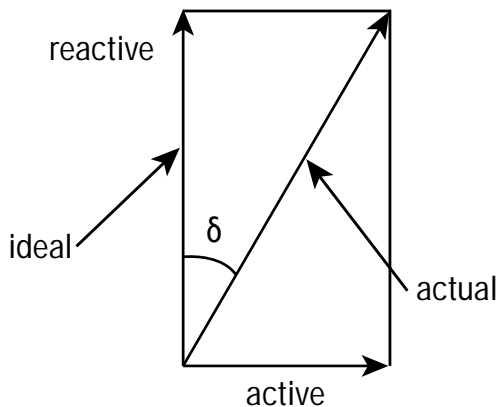
Effective capacitance value is derived from the impedance curve, as long as impedance is still in the range where the capacitance component is dominant.

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$$C = \frac{1}{2\pi f Z} \quad C = \text{capacitance (F)}$$

$$f = \text{frequency (Hz)}$$

ESR is a measure of the loss in the capacitor. It is the resistance in the series circuit of the capacitor. ESR is measured in ohms (Ω) and is typically in the range of 0.1 to 100 Ω. ESR is thought of as a measurement of the gap between an actual and ideal capacitor.



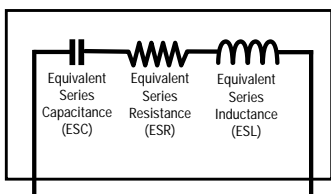
ESR is a measure of the loss in the capacitor. It is the resistance in the series circuit of the capacitor. ESR is measured in ohms (Ω) and is typically in the range of 0.1 to 100 Ω.

ESC = Equivalent series capacitance
 ESR = Equivalent series resistance

) UYMZEPIRX 7IVMIW -RHYGXERGI) 70

) UYMZEPIRX WIVMIW MRHYGXERGI SV WIPJ MRHYGXERGI VIWYPXW JVS

Capacitor Equivalent Internal Circuit



) UYMZEPIRX 7IVMIW 6IWMWXERGI) 76

Equivalent series resistance is the resistive component of the equivalent series circuit. ESR value depends on frequency.
XIQTIVEXYVI ERH MW VIPEXIH XS XLI XER ¾ F] XLI JSPPS[MRK IUYEXM

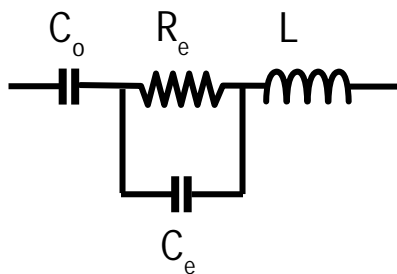
$$ESR = \frac{XER}{\omega C} \quad \text{ESC} = \text{Equivalent series capacitance}$$

f = Frequency (Hz)

Tolerance limits of the rated capacitance must be taken into account when calculating this value.

- QTIHERGI >

Impedance of an electrolytic capacitor results from a circuit formed by the following individual equivalent series components:



C₀ = Aluminum oxide capacitance (surface and thickness of the dielectric.)

R_e = Resistance of electrolyte and paper mixture (other resistances not depending on the frequency are not considered plates, etc.)

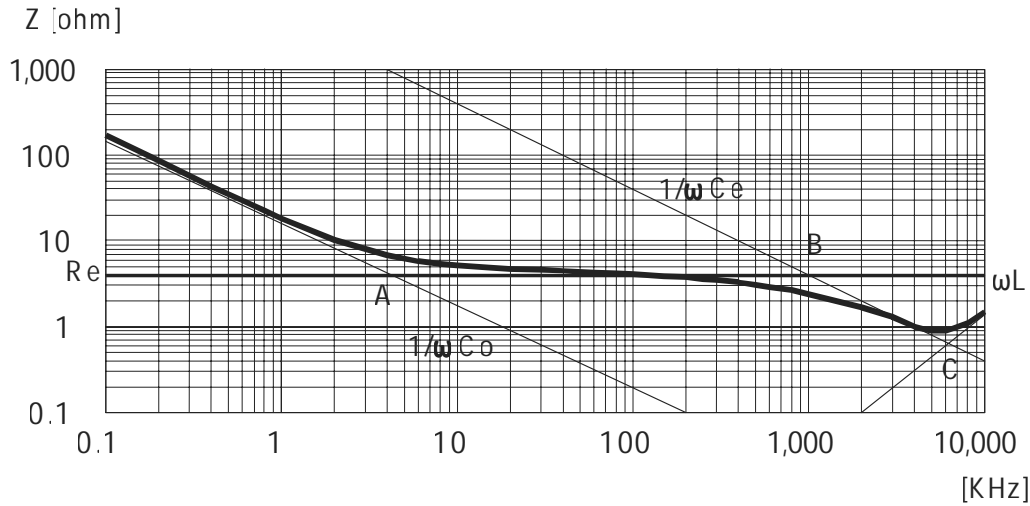
C_e = Electrolyte soaked paper capacitance.

L = Inductive reactance of the capacitor winding and terminals.

Impedance of an electrolytic capacitor is not a constant quantity that retains its value under all conditions; it changes depending on frequency and temperature.

Impedance as a function of frequency (sinusoidal waveform) for a certain temperature can be represented as follows:

- QTIHERGI > GSRX ... H



- Capacitive reactance predominates at low frequencies.
 - As frequency increases, the magnitude of the electrolyte resistance (A) decreases.
 - At even higher frequencies, resistance of the electrolyte predominates (B).
 - Capacitive and inductive reactance mutually cancel each other out.
 - At very high frequencies, an increase in impedance occurs due to the inductive reactance (C).
- Generally speaking, it can be estimated that C ...

Impedance as a function of frequency (sinusoidal waveform) for different temperature values can be represented as follows (typical values):

01EOEKI 'YVVIRX 0'

(YI XS XLI EPYQMRYQ S\MHI PE]IV XLEX WIVZIW EW E HMIPIGXVMG E
 been applied for long periods. This current is called leakage current.

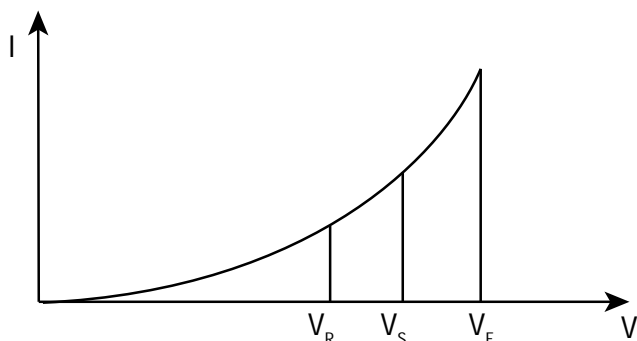
% LMKL PIEOEKI GYVVIRX ½ S[W EJXIV ETTP]MRK ZSPXEKI XS XLI GETE
 prolonged storage without any applied voltage. In the course of continuous operation, the leakage current will decrease
 reach an almost constant value.

After a voltage-free storage the oxide layer may deteriorate, especially at a high temperature. Since there are no leakage
 currents to transport oxygen ions to the anode, the oxide layer is not regenerated. The result is that a higher than normal

PIEOEKI GYVVIRX [MPP ½ S[[LIR ZSPXEKI MW ETTPMIH EJXIV TVSPSRK

As the oxide layer is regenerated in use, the leakage current will gradually decrease to its normal level.

The relationship between the leakage current and voltage applied at constant temperature can be shown schematically
 follows:



Where:

V_F = Forming voltage

If this level is exceeded, a large quantity of heat and gas will be generated and the capacitor could be damaged.

V_R = Rated voltage

This level represents the top of the linear part of the curve.

V_S = Surge voltage

This lies between V_R and V_F . The capacitor can be subjected to V_S for short periods only.

Electrolytic capacitors are subjected to a reforming process before acceptance testing. The purpose of this preconditioning
 is to ensure that the same initial conditions are maintained when comparing different products.

6MTTPI 'YVVIRX 6'

The maximum ripple current value depends on:

- Ambient temperature
- Surface area of the capacitor (heat dissipation area)
- XER ¾ SV) 7 6
- Frequency

The capacitor's life depends on the thermal stress.

*VIUYIRGJ (ITIRHIRGI SJ XLI 6MTTPI 'YVVIRX
)76 ERH XLYW XLI XEUYIRB JTSBHXISREXTIPJMH ZSPXEKI 8LMW MRHMGE
 a function of the frequency.

8IQTIVEXYVI (ITIRHIRGI SJ XLI 6MTTPI 'YVVIRX
 8LI HEXE WLIIX WTIGM¼IW QE\MQYQ VMTTPI GYVVIRX EX XLI YTTIV GE

)\TIGXIH 0MJI 'EPGYPEXMSR

Expected life depends on operating temperature according to the following formula: $(\frac{T_0 - T}{T_0})^{10} \times 2$

Where:

- L: Expected life
- Lo: Load life at a maximum permissible operating temperature
- T: Actual operating temperature
- To: Maximum permissible operating temperature

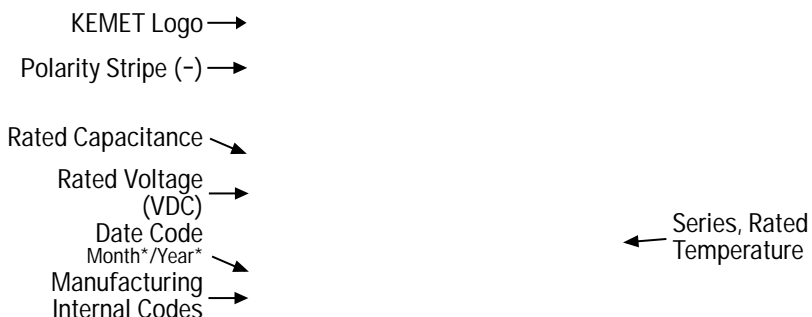
This formula is applicable between 40°C and To.

%GXYEP 3TIVEXMRK 8I)\TIGXIH 0MJI 'EPGYPEXMSR 'LEVX
)\TIGXIH PMJI L

4EGOEKMRK 5YERXMXMIW

Size 'SHI	(MEQI QQ	X0MRKXL QQ	&YPO		%YXS MRWI V XMSR	
			7XERHEVH 0IEHW	YX 0IEHW	%QQS	8ETI 6IIP
B2	4	7	10,000	15,000	2,500	3,000
C2	5	7	10,000	15,000	2,000	2,600
E2	6.3	7	10,000	15,000	2,000	2,200
+	8	7	6,000	8,000	1,000	1,500
+	8	9	6,000	8,000	1,000	1,500

Marking



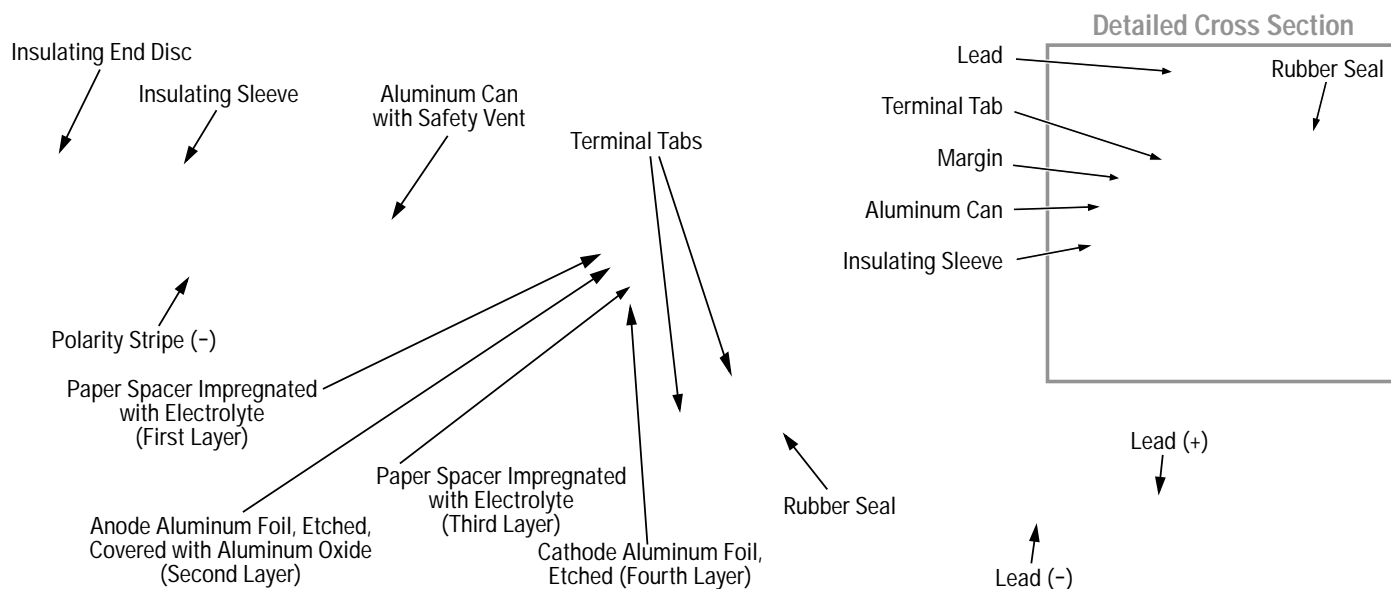
*Y = Year

Code	01	02	03	04	05	06	07	08	09
Year	2011	2012	2013	2014	2015	2016	2017	2018	2019

*M = Month

Code	01	02	03	04	05	06	07	08	09	10	11	12
Month	1	2	3	4	5	6	7	8	9	10	11	12

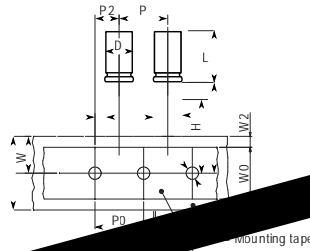
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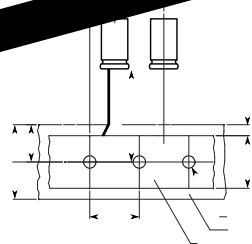
8ETMRK JSV %YXSQEXMG -RWIVXMSR 1EGLMRIW

Formed to 2.5 mm
(Lead and packaging code LA and FA)

Formed to 5 mm
(Lead and packaging code JA and DA)



Straight Leads (Diameter...)
Lead and packaging code...

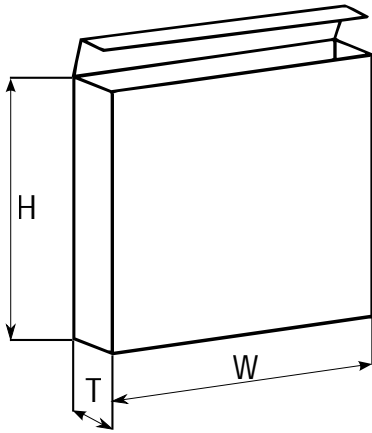


(MQIRW) QQ	D	SRW L	p	d	P	P0	P1	P2	W	W0	W1	W2	,	,	I	D0	X
Tolerance	+0.5		-	0.05	±0	0.3	0.7	±3	-	0.5	Maximum	Maximum	0.7	0.5	Maximum	0.2	0.2
Formed to 2.5 mm	4	5-7	2.5	0.45	12.7	12.7	5.1	0.7	18	12	11	3	16	18.5		4	0.7
	5	μ	2.5	0.45	12.7	12.7	5.1	0.7	18	12	11	3	16	18.5		4	0.7
		>7	2.5	0.5	12.7	12.7	5.1	0.7	18	12	11	3	16	18.5		4	0.7
Formed to 5 mm	4	5-7	5	0.45	12.7	12.7	5.6	0.7	18	12	11	3	16	18.5		4	0.7
		μ	5	0.45	12.7	12.7	5.6	0.7	18	12	11	3	16	18.5		4	0.7
		>7	5	0.5	12.7	12.7	5.6	0.7	18	12	11	3	16	18.5		4	0.7
	6	μ	5	0.5	12.7	12.7	5.6	0.7	18	12	11	3	16	18.5		4	0.7
		>7	5	0.5	12.7	12.7	5.6	0.7	18	12	11	3	16	18.5		4	0.7
		μ	5	0.5	12.7	12.7	5.6	0.7	18	12	11	3	16	18.5		4	0.7
8	μ	5	0.5	12.7	12.7	5.6	0.7	18	12	11	3	16	18.5		4	0.7	
	>7	5	0.5	12.7	12.7	5.6	0.7	18	12	11	3	16	18.5		4	0.7	
	μ	5	0.5	12.7	12.7	5.6	0.7	18	12	11	3	16	18.5		4	0.7	
Straight leads	4	5-7	1.5	0.45	12.7	12.7	5.6	0.7	18	12	11	3	18.5			4	0.7
		μ	2	0.45	12.7	12.7	5.6	0.7	18	12	11	3	18.5			4	0.7
		>7	2	0.5	12.7	12.7	5.6	0.7	18	12	11	3	18.5			4	0.7
	6	μ	2.5	0.5	12.7	12.7	5.1	0.7	18	12	11	3	18.5			4	0.7
		>7	2.5	0.5	12.7	12.7	5.1	0.7	18	12	11	3	18.5			4	0.7
	8	μ	3	0.5	12.7	12.7	4.6	0.7	18	12	11	3	18.5			4	0.7
		>7	3	0.5	12.7	12.7	4.6	0.7	18	12	11	3	18.5			4	0.7
	10	12-25	5	0.6	12.7	12.7	5.6	0.7	18	12	11	3	18.5		1	4	1
	12	15-25	5	0.6	15	15	5.6	0.7	18	12	11	3	18.5		1	4	1
	13		5	0.6	15	15	5.6	0.7	18	12	11	3	18.5		1	4	1
5			0.6	15	15	5.6	0.7	18	12	11	3	18.5		1	4	1	
16	5		0.8	15	15	5.6	0.7	18	12	11	3	18.5		1	4	1	
18	5		0.8	15	15	5.6	0.7	18	12	11	3	18.5		1	4	1	

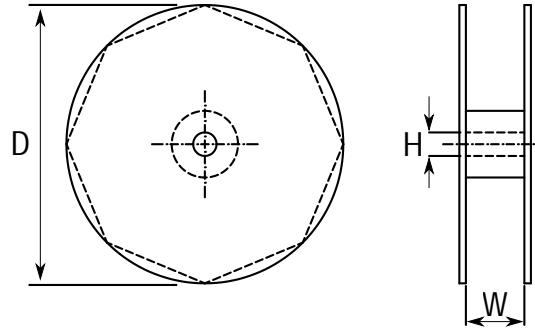
01EH 8ETMRK

4EGOEKMRK

Ammo Box



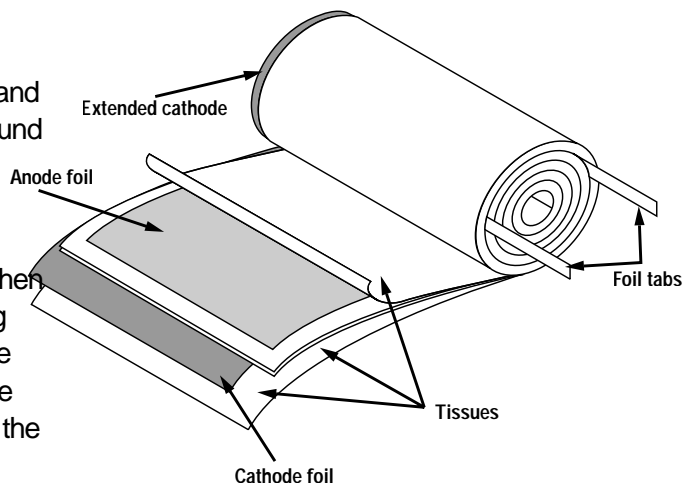
Reel



'E W I 7 M ^	% Q Q S		6 I I P			
	,	W	8	D	,	W
		Maximum	Maximum	±2	±0.5	-
4	230	340	42	350	30	50
5 x 5 - 7	230	340	42			

' S R W X V Y G X M S R (E X E

The manufacturing process begins with the anode foil being electrochemically etched to increase the surface area and then “formed” to produce the aluminum oxide layer. Both the anode and cathode foils are then interleaved with absorbent paper and wound into a cylinder. During the winding process, aluminum tabs are attached to each foil to provide the electrical contact.



The deck, complete with terminals, is attached to the tabs and then folded down to rest on top of the winding. The complete winding is impregnated with electrolyte before being housed in a suitable container, usually an aluminum can, and sealed. Throughout the process, all materials inside the housing must be maintained at the highest purity and be compatible with the electrolyte.

Each capacitor is aged and tested before being sleeved and packed. The purpose of aging is to repair any damage in the oxide layer and thus reduce the leakage current to a very low level. Aging is normally carried out at the rated temperature of the capacitor and is accomplished by applying voltage to the device while carefully controlling the supply current. The process may take several hours to complete.

Damage to the oxide layer can occur due to variety of reasons:

- Slitting of the anode foil after forming
- Attaching the tabs to the anode foil
- Minor mechanical damage caused during winding

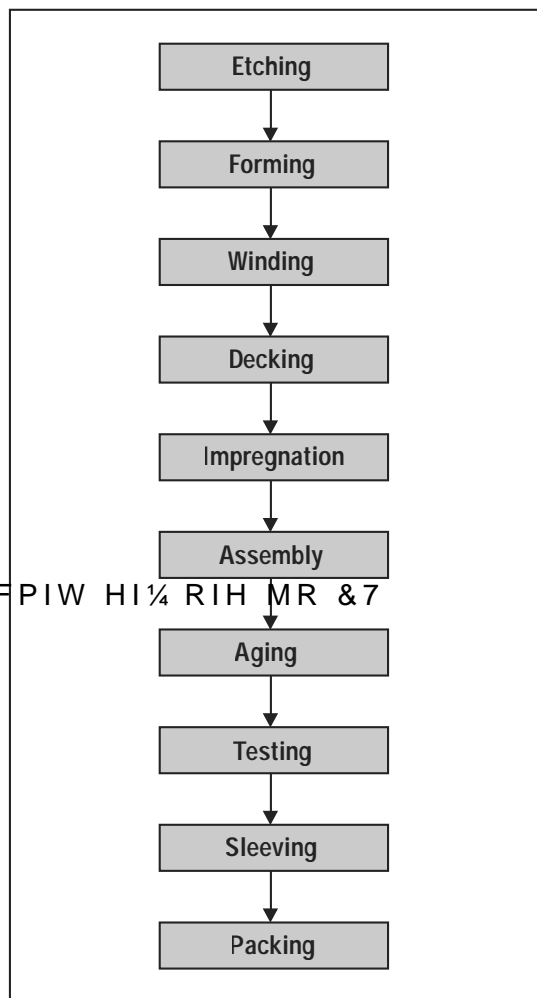
A sample from each batch is taken by the quality department after completion of the production process. This sample size is controlled

F] XLI YWI SJ VIGSKRM^IH WEQTPMRK XEFPIW HI¼ RIH MR & 7

The following tests are applied and may be varied at the request of the customer. In this case the batch, or special procedure, will determine the course of action.

Electrical:

- Leakage current
- Capacitance
- ESR
- Impedance
- Tc1(n)-8. ()TJ T*225 Cnncn



- TlhD7nl1A00140014e

)1)8)PIGXVSRMGW 'SVTSVEXMSR 7EPIW 3¾ GIW

*SV E GSQTPIXI PMWX SJ SYV KPSFEP WEPIW S¾ GIW TPIEWI ZMWMX

(MWGPQMIV

%PP TVSHYGX WTIGM¼ GEXMSRW WXEXIQIRXW MRJSVQEXMSR ERH HEXE GSPPIGXMZIP] XLI ^
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(such as installation of protective circuitry or redundancies) in order to ensure that the failure of an electrical component does not result in a risk of personal inju
property damage.

Although all product-related warnings, cautions and notes must be observed, the customer should not assume that all safety measures are indicted or that othe
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