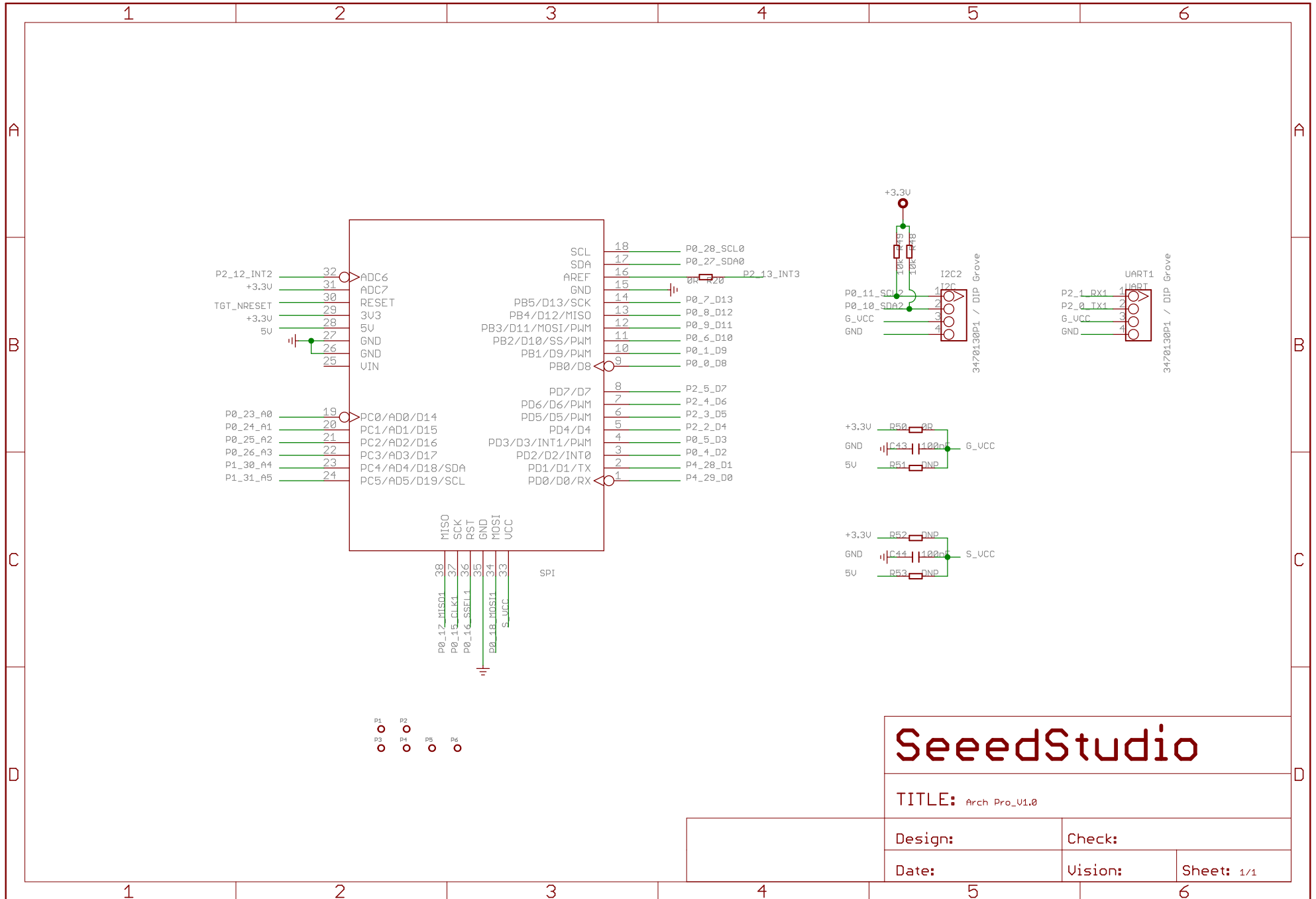


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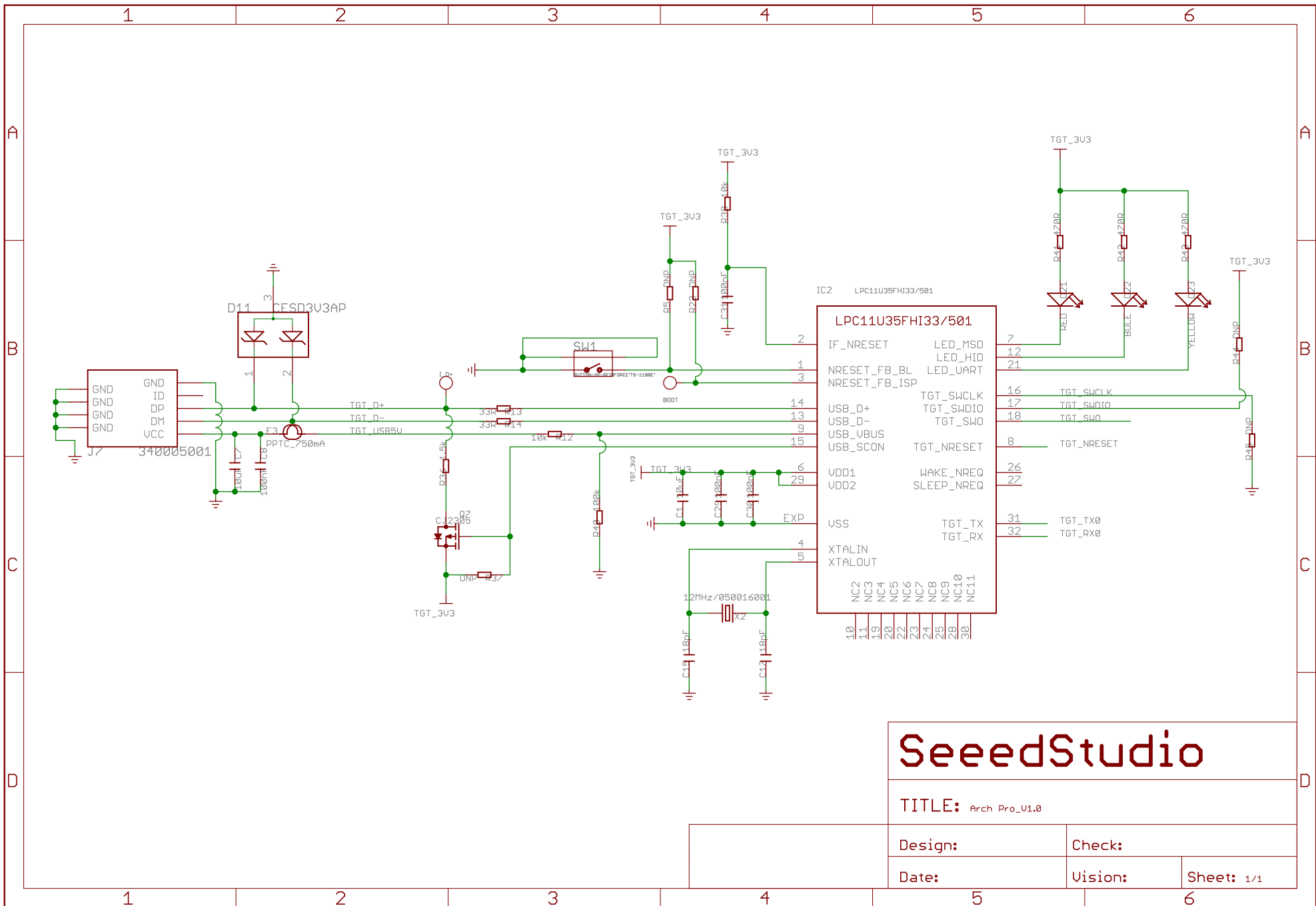
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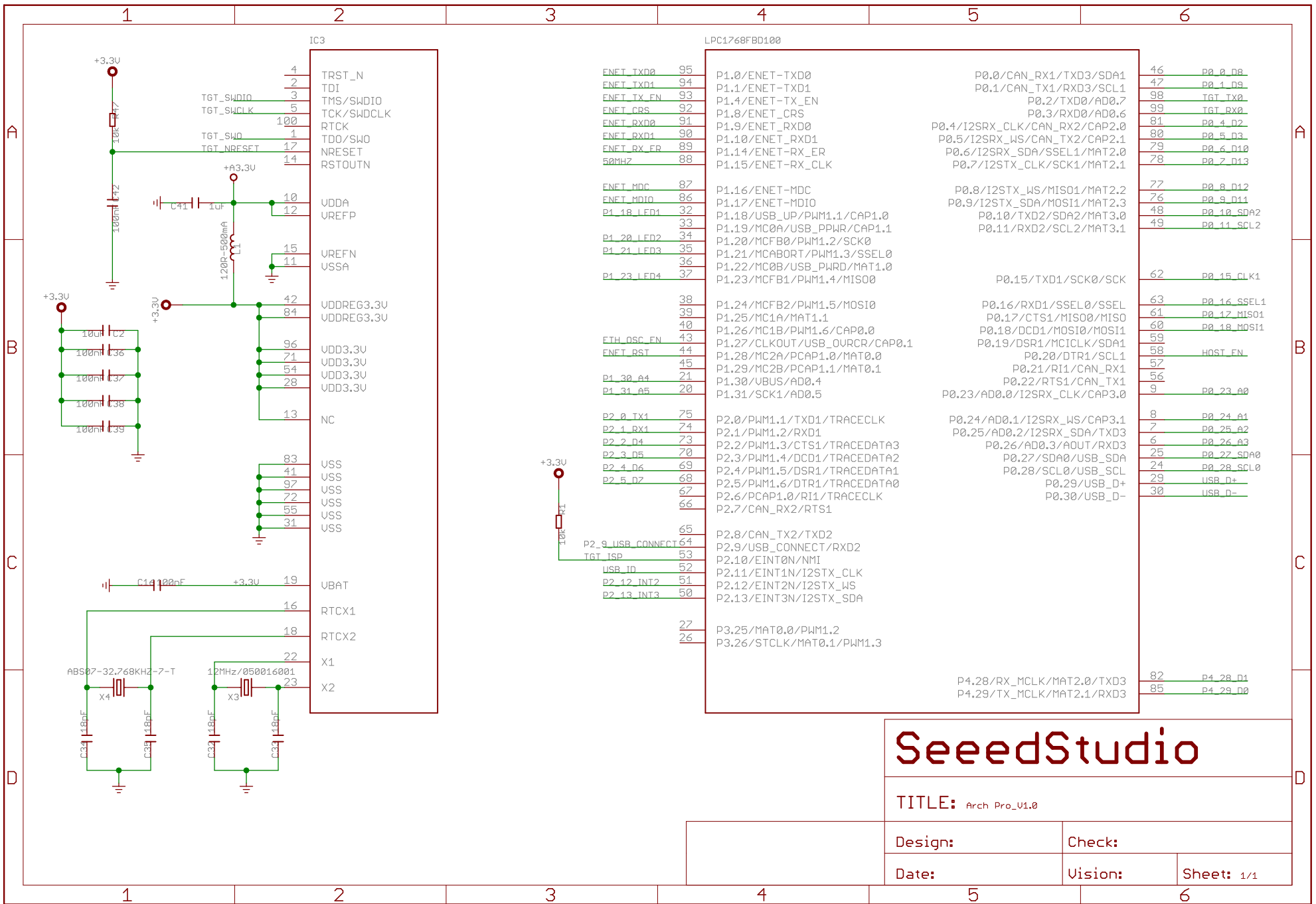
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IC3

4	TRST_N
2	TDI
3	TMS/SWDIO
5	TCK/SWDCLK
100	RTCK
1	TDO/SWO
17	TGT_NRESET
14	RSTOUTN
10	UDDA
12	UREFP
15	UREFN
11	USSA
42	UDDREG3.3V
84	UDDREG3.3V
96	UDD3.3V
71	UDD3.3V
54	UDD3.3V
28	UDD3.3V
13	NC
83	USS
41	USS
97	USS
72	USS
55	USS
31	USS
19	VBAT
16	RTCX1
18	RTCX2
22	X1
23	X2

LPC1768FBD100

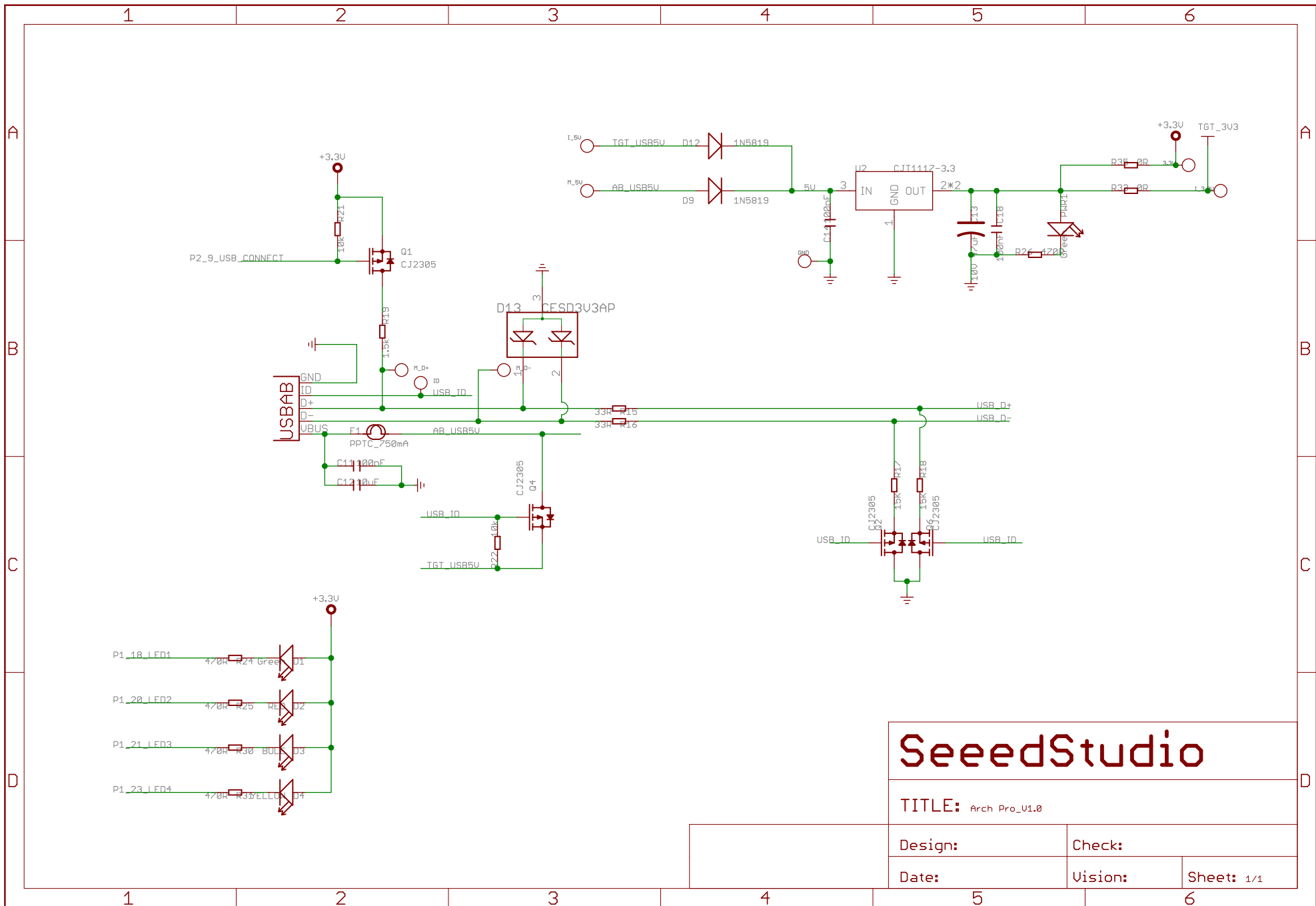
ENET_TXD0	95
ENET_TXD1	94
ENET_TX_EN	93
ENET_CRD3	92
ENET_RXD0	91
ENET_RXD1	90
ENET_RX_ER	89
50MHZ	88
ENET_MDC	87
ENET_MDIO	86
P1_18_LFD1	32
P1_20_LFD2	34
P1_21_LFD3	35
P1_23_LFD4	36
ETH_OSC_EN	43
ENET_RST	44
P1_30_A4	21
P1_31_A5	20
P2_0_TX1	75
P2_1_RX1	74
P2_2_D4	73
P2_3_D5	70
P2_4_D6	69
P2_5_D7	68
P2_6	67
P2_7	66
P2_8/CAN_TX2/TXD2	65
P2_9/USB_CONNECT/RXD2	64
TGT_ISP	53
USB_ID	52
P2_12_INT2	51
P2_13_INT3	50
P3_25/MAT0.0/PWM1.2	27
P3_26/STCLK/MAT0.1/PWM1.3	26

P0.0/CAN_RX1/TXD3/SDA1	46	P0.0_PA
P0.1/CAN_TX1/RXD3/SCL1	47	P0.1_PA
P0.2/TXD0/AD0.7	98	TGT_TX0
P0.3/RXD0/AD0.6	99	TGT_RX0
P0.4/I2SRX_CLK/CAN_RX2/CAP2.0	81	P0.4_P2
P0.5/I2SRX_WS/CAN_TX2/CAP2.1	80	P0.5_P3
P0.6/I2SRX_SDA/SSEL1/MAT2.0	79	P0.6_P10
P0.7/I2STX_CLK/SCK1/MAT2.1	78	P0.7_P13
P0.8/I2STX_WS/MISO1/MAT2.2	77	P0.8_P12
P0.9/I2STX_SDA/MOSI1/MAT2.3	76	P0.9_P11
P0.10/TXD2/SDA2/MAT3.0	48	P0.10_SDA2
P0.11/RXD2/SCL2/MAT3.1	49	P0.11_SCL2
P0.15/TXD1/SCK0/SCK	62	P0.15_CLK1
P0.16/RXD1/SSEL0/SSEL	63	P0.16_SSEL1
P0.17/CTS1/MISO0/MISO	61	P0.17_MISO1
P0.18/DCD1/MOSI0/MOSI1	60	P0.18_MOSI1
P0.19/DSR1/MCICK/SCL1	59	HOST_EN
P0.20/DTR1/SCL1	58	HOST_EN
P0.21/RI1/CAN_RX1	57	
P0.22/RTS1/CAN_TX1	56	
P0.23/AD0.0/I2SRX_CLK/CAP3.0	9	P0.23_A0
P0.24/AD0.1/I2SRX_WS/CAP3.1	8	P0.24_A1
P0.25/AD0.2/I2SRX_SDA/TXD3	7	P0.25_A2
P0.26/AD0.3/AOUT/RXD3	6	P0.26_A3
P0.27/SDA0/USB_SDA	25	P0.27_SDA0
P0.28/SCL0/USB_SCL	24	P0.28_SCL0
P0.29/USB_D+	29	USB_D+
P0.30/USB_D-	30	USB_D-
P4.28/RX_MCLK/MAT2.0/TXD3	82	P4.28_D1
P4.29/TX_MCLK/MAT2.1/RXD3	85	P4.29_D0

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